

Prüfbericht-Nr.: Test report no.:	CN22DK54 002	Auftrags-Nr.: Order no.:	169818608	Seite 1 von 44 Page 1 of 44
Kunden-Referenz-Nr.: Client reference no.:	2026754	Auftragsdatum: Order date:	2024.07.24	
Auftraggeber: Client:	Guangzhou Sanjing Electric Co., Ltd. No.9, Lizhishan Road, Science City, Guangzhou Hightech Zone, Guangdong, P.R. China			
Prüfgegenstand: Test item:	PV Grid-connected Inverter			
Bezeichnung / Typ-Nr.: Identification / Type no.:	C6-50K-T6-LV-40, C6-60K-T9-LV-40, C6-70K-T12-LV-40, C6-75K-T12-LV-40			
Auftrags-Inhalt: Order content:	TÜV Rheinland AK certificate			
Prüfgrundlage: Test specification:	IEC 61727: 2004, IEC 62116: 2014			
Wareneingangsdatum: Date of sample receipt:	2024.12.20			
Prüfmuster-Nr.: Test sample no:	A003905686-001			
Prüfzeitraum: Testing period:	2024.12.01 – 2024.12.20			
Ort der Prüfung: Place of testing:	Guangzhou Sanjing Electric Co., Ltd. No.9, Lizhishan Road, Science City, Guangzhou Hightech Zone, Guangdong, P.R. China			
Prüflaboratorium: Testing laboratory:	TÜV Rheinland (Shenzhen) Co., Ltd.			
Prüfergebnis*: Test result*:	Pass			
geprüft von: tested by:	 Hazel He		genehmigt von: authorized by:	 Lucas Zheng
Datum: Date:	2025.03.05		Ausstellungsdatum: Issue date:	2025.03.06
Stellung / Position:	Sachverständige(r)/Expert		Stellung / Position:	Sachverständige(r)/Expert
Sonstiges / Other:	1. For issuing TÜV Rheinland AK certificate. 2. This report is based on CN22DK54 001. Corresponding tests had been performed. See report for details. 3. This report does not evidence compliance of the provided sample with the relevant standards but only with the referred tests. This test report documents the findings of examination conducted on the delivered product mentioned above only. This report does not entitle the applicant to carry any safety mark on this or similar products. Further for sales or other application purposes of the tested product, any reference to TÜV Rheinland or a test through TÜV Rheinland is only permissible with prior written consent of TÜV Rheinland.			
Zustand des Prüfgegenstandes bei Anlieferung: Condition of the test item at delivery:	Prüfmuster vollständig und unbeschädigt Test item complete and undamaged			
* Legende:	P(ass) = entspricht o.g. Prüfgrundlage(n) F(ail) = entspricht nicht o.g. Prüfgrundlage(n) N/A = nicht anwendbar N/T = nicht getestet			
* Legend:	P(ass) = passed a.m. test specification(s) F(ail) = failed a.m. test specification(s) N/A = not applicable N/T = not tested			
Dieser Prüfbericht bezieht sich nur auf das o.g. Prüfmuster und darf ohne Genehmigung der Prüfstelle nicht auszugsweise vervielfältigt werden. Dieser Bericht berechtigt nicht zur Verwendung eines Prüfzeichens. <i>This test report only relates to the a. m. test sample. Without permission of the test center this test report is not permitted to be duplicated in extracts. This test report does not entitle to carry any test mark.</i>				

Prüfbericht-Nr.: CN22DK54 002
Test report no.:

Seite 2 von 44
Page 2 of 44

Anmerkungen
Remarks

1	Alle eingesetzten Prüfmittel waren zum angegebenen Prüfzeitraum gemäß eines festgelegten Kalibrierungsprogramms unseres Prüflabors kalibriert. Sie entsprechen den in den Prüfprogrammen hinterlegten Anforderungen. Die Rückverfolgbarkeit der eingesetzten Prüfmittel ist durch die Einhaltung der Regelungen unseres Managementsystems gegeben. Detaillierte Informationen bezüglich Prüfkonditionen, Prüfequipment und Messunsicherheiten sind im Prüflabor vorhanden und können auf Wunsch bereitgestellt werden. <i>The equipment used during the specified testing period was calibrated according to our test laboratory calibration program. The equipment fulfils the requirements included in the relevant standards. The traceability of the test equipment used is ensured by compliance with the regulations of our management system. Detailed information regarding test conditions, equipment and measurement uncertainty is available in the test laboratory and could be provided on request.</i>
2	Wie vertraglich vereinbart, wurde dieses Dokument nur digital unterzeichnet. Der TÜV Rheinland hat nicht überprüft, welche rechtlichen oder sonstigen diesbezüglichen Anforderungen für dieses Dokument gelten. Diese Überprüfung liegt in der Verantwortung des Benutzers dieses Dokuments. Auf Verlangen des Kunden kann der TÜV Rheinland die Gültigkeit der digitalen Signatur durch ein gesondertes Dokument bestätigen. Diese Anfrage ist an unseren Vertrieb zu richten. Eine Umweltgebühr für einen solchen zusätzlichen Service wird erhoben. Informationen zur Verifizierung der Authentizität unserer Dokumente erhalten Sie auf folgender Webseite: go.tuv.com/digital-signature <i>As contractually agreed, this document has been signed digitally only. TÜV Rheinland has not verified and unable to verify which legal or other pertaining requirements are applicable for this document. Such verification is within the responsibility of the user of this document. Upon request by its client, TÜV Rheinland can confirm the validity of the digital signature by a separate document. Such request shall be addressed to our Sales department. An environmental fee for such additional service will be charged. For information on verifying the authenticity of our documents, please visit the following website: go.tuv.com/digital-signature</i>
3	Prüfklausel mit der Note * wurden an qualifizierte Unterauftragnehmer vergeben und sind unter der jeweiligen Prüfklausel des Berichts beschrieben. Abweichungen von Prüfspezifikation(en) oder Kundenanforderungen sind in der jeweiligen Prüfklausel im Bericht aufgeführt. <i>Test clauses with remark of * are subcontracted to qualified subcontractors and described under the respective test clause in the report.</i> <i>Deviations of testing specification(s) or customer requirements are listed in specific test clause in the report.</i>
4	Die Entscheidungsregel für Konformitätserklärungen basierend auf numerischen Messergebnissen in diesem Prüfbericht basiert auf der "Null-Grenzwert-Regel" und der "Einfachen Akzeptanz" gemäß ILAC G8:2019 und IEC Guide 115:2021, es sei denn, in der auf Seite 1 dieses Berichts genannten angewandten Norm ist etwas anderes festgelegt oder vom Kunden gewünscht. Dies bedeutet, dass die Messunsicherheit nicht berücksichtigt wird und daher auch nicht im Prüfbericht angegeben wird. Zu weiteren Informationen bezüglich des Risikos durch diese Entscheidungsregel siehe ILAC G8:2019. <i>The decision rule for statements of conformity, based on numerical measurement results, in this test report is based on the "Zero Guard Band Rule" and "Simple Acceptance" in accordance with ILAC G8:2019 and IEC Guide 115:2021, unless otherwise specified in the applied standard mentioned on Page 1 of this report or requested by the customer. This means that measurement uncertainty is not taken in account and hence also not declared in the test report. For additional information to the resulting risk based of this decision rule please refer to ILAC G8:2019.</i>

TEST REPORT IEC 61727: 2004 Photovoltaic (PV) systems– Characteristics of the utility interface IEC 62116: 2014 Utility-interconnected photovoltaic inverters-Test procedure of islanding prevention measures	
Report Reference No.: CN22DK54 002	
Tested by (name + signature): See cover page	
Witnessed by (name + signature) ..: See cover page	
Supervised by (name + signature) .: See cover page	
Approved by (name + signature).....: See cover page	
Date of issue.....: See cover page	
Testing Laboratory.....: TÜV Rheinland (Shenzhen) Co., Ltd.	
Address: 1-5F, Block 5, No. 1100, Huanli Road, Yungu Community, Xinhua Street, Guangming District, Shenzhen, Guangdong Province	
Testing location/ procedure: CBTL ☐ TMP ☒ WMT ☐ SMT ☐ RMT ☐ CCATL ☐	
Testing location/ address: Guangzhou Sanjing Electric Co., Ltd. No.9, Lishishan Road, Science City, Guangzhou Hightech Zone, Guangdong P.R. China	
Applicant's name: See cover page	
Address: See cover page	
Test specification: Standard: IEC 61727: 2004, IEC 62116: 2014 Test procedure: AK Non-standard test method.....: N/A	
Test Report Form No.....: IEC 61727A Test Report Form(s) Originator: TÜV Rheinland Group Master TRF.....: 2014-02	
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Test item description	: PV Grid-connected Inverter
Trade Mark	: 
Manufacturer	: Same as client
Model/Type reference	: See cover page
Ratings	: See model list

Testing procedure and testing location:		
☒	Testing Laboratory:	TÜV Rheinland (Shenzhen) Co., Ltd.
☐	Associated CB Test Laboratory:	
Testing location/ address		
	Tested by (name + signature)	See cover page
	Approved by (+ signature)	See cover page
☒	Testing procedure: TMP	
	Tested by (name + signature)	Hazel He See cover page
	Approved by (+ signature)	Lucas Zheng See cover page
Testing location/ address: Guangzhou Sanjing Electric Co., Ltd.		
No.9, Lizhishan Road, Science City, Guangzhou Hightech Zone, Guangdong P.R. China		
☐	Testing procedure: WMT	
	Tested by (name + signature)	
	Witnessed by (+ signature)	
	Approved by (+ signature)	
Testing location/ address		
☐	Testing procedure: SMT	
	Tested by (name + signature)	
	Approved by (+ signature)	
	Supervised by (+ signature)	
Testing location/ address		
☐	Testing procedure: RMT	
	Tested by (name + signature)	
	Approved by (+ signature)	
	Supervised by (+ signature)	
Testing location/ address		

Copy of marking plate:

60mm

135mm



Guangzhou Sanjing Electric Co., Ltd.
Tel: +86(20)68608588 Fax: +86(20)68608589
Web: www.saj-electric.com E-mail: service@saj-electric.com

PV Grid-connected Inverter
Model: C6-50K-T6-LV-40

PV Input	
Max. Input Voltage	1100V
MPPT Voltage Range	180-1000V
Max. Input Current	6*40A
Short Circuit Current	6*50A

AC Output	
Rated Output Voltage	3/N/PE 127/220V
Rated Output Current	3*131.2A
Max. Output Current	3*144.3A
Rated Output Frequency	50/60Hz
Rated Output Power	50000W
Max. Apparent Power	55000VA
Power Factor Range	0.8 Leading...0.8 Lagging

Operating Temperature Range: -30°C~60°C
Protective Class: I
Overvoltage Category: II (DC), III (AC)
Ingress protection: IP66
Inverter Topology: Non-isolated
IEC/EN62109-1/2, EN61000-6-2/4







S/N

P/C

MADE IN CHINA

60mm

135mm



Guangzhou Sanjing Electric Co., Ltd.
Tel: +86(20)68608588 Fax: +86(20)68608589
Web: www.saj-electric.com E-mail: service@saj-electric.com

PV Grid-connected Inverter
Model: C6-60K-T9-LV-40

PV Input	
Max. Input Voltage	1100V
MPPT Voltage Range	180-1000V
Max. Input Current	9*40A
Short Circuit Current	9*50A

AC Output	
Rated Output Voltage	3/N/PE 127/220V
Rated Output Current	3*157.5A
Max. Output Current	3*157.5A
Rated Output Frequency	50/60Hz
Rated Output Power	60000W
Max. Apparent Power	60000VA
Power Factor Range	0.8 Leading...0.8 Lagging

Operating Temperature Range: -30°C~60°C
Protective Class: I
Overvoltage Category: II (DC), III (AC)
Ingress protection: IP66
Inverter Topology: Non-isolated
IEC/EN62109-1/2, EN61000-6-2/4







S/N

P/C

MADE IN CHINA

 Guangzhou Sanjing Electric Co., Ltd. Tel: +86(20)66608588 Fax: +86(20)66608589 Web: www.saj-electric.com E-mail: service@saj-electric.com PV Grid-connected Inverter Model: C6-70K-T12-LV-40 <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <th colspan="2" style="text-align: center;">PV Input</th> </tr> <tr> <td style="width: 50%;">Max. Input Voltage</td> <td style="width: 50%;">1100V</td> </tr> <tr> <td>MPPT Voltage Range</td> <td>180-1000V</td> </tr> <tr> <td>Max. Input Current</td> <td>12*40A</td> </tr> <tr> <td>Short Circuit Current</td> <td>12*50A</td> </tr> <tr> <th colspan="2" style="text-align: center;">AC Output</th> </tr> <tr> <td>Rated Output Voltage</td> <td>3/N/PE 127/220V</td> </tr> <tr> <td>Rated Output Current</td> <td>3*183.7A</td> </tr> <tr> <td>Max. Output Current</td> <td>3*183.7A</td> </tr> <tr> <td>Rated Output Frequency</td> <td>50/60Hz</td> </tr> <tr> <td>Rated Output Power</td> <td>70000W</td> </tr> <tr> <td>Max. Apparent Power</td> <td>70000VA</td> </tr> <tr> <td>Power Factor Range</td> <td>0.8 Leading...0.8 Lagging</td> </tr> <tr> <td colspan="2">Operating Temperature Range: -30°C~60°C</td> </tr> <tr> <td colspan="2">Protective Class: I</td> </tr> <tr> <td colspan="2">Overvoltage Category: II (DC), III (AC)</td> </tr> <tr> <td colspan="2">Ingress protection: IP66</td> </tr> <tr> <td colspan="2">Inverter Topology: Non-isolated</td> </tr> <tr> <td colspan="2">IEC/EN62109-1/2, EN61000-6-2/4</td> </tr> </table>   S/N P/C MADE IN CHINA	PV Input		Max. Input Voltage	1100V	MPPT Voltage Range	180-1000V	Max. Input Current	12*40A	Short Circuit Current	12*50A	AC Output		Rated Output Voltage	3/N/PE 127/220V	Rated Output Current	3*183.7A	Max. Output Current	3*183.7A	Rated Output Frequency	50/60Hz	Rated Output Power	70000W	Max. Apparent Power	70000VA	Power Factor Range	0.8 Leading...0.8 Lagging	Operating Temperature Range: -30°C~60°C		Protective Class: I		Overvoltage Category: II (DC), III (AC)		Ingress protection: IP66		Inverter Topology: Non-isolated		IEC/EN62109-1/2, EN61000-6-2/4		 Guangzhou Sanjing Electric Co., Ltd. Tel: +86(20)66608588 Fax: +86(20)66608589 Web: www.saj-electric.com E-mail: service@saj-electric.com PV Grid-connected Inverter Model: C6-75K-T12-LV-40 <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <th colspan="2" style="text-align: center;">PV Input</th> </tr> <tr> <td style="width: 50%;">Max. Input Voltage</td> <td style="width: 50%;">1100V</td> </tr> <tr> <td>MPPT Voltage Range</td> <td>180-1000V</td> </tr> <tr> <td>Max. Input Current</td> <td>12*40A</td> </tr> <tr> <td>Short Circuit Current</td> <td>12*50A</td> </tr> <tr> <th colspan="2" style="text-align: center;">AC Output</th> </tr> <tr> <td>Rated Output Voltage</td> <td>3/N/PE 127/220V</td> </tr> <tr> <td>Rated Output Current</td> <td>3*196.8A</td> </tr> <tr> <td>Max. Output Current</td> <td>3*196.8A</td> </tr> <tr> <td>Rated Output Frequency</td> <td>50/60Hz</td> </tr> <tr> <td>Rated Output Power</td> <td>75000W</td> </tr> <tr> <td>Max. Apparent Power</td> <td>75000VA</td> </tr> <tr> <td>Power Factor Range</td> <td>0.8 Leading...0.8 Lagging</td> </tr> <tr> <td colspan="2">Operating Temperature Range: -30°C~60°C</td> </tr> <tr> <td colspan="2">Protective Class: I</td> </tr> <tr> <td colspan="2">Overvoltage Category: II (DC), III (AC)</td> </tr> <tr> <td colspan="2">Ingress protection: IP66</td> </tr> <tr> <td colspan="2">Inverter Topology: Non-isolated</td> </tr> <tr> <td colspan="2">IEC/EN62109-1/2, EN61000-6-2/4</td> </tr> </table>   S/N P/C MADE IN CHINA	PV Input		Max. Input Voltage	1100V	MPPT Voltage Range	180-1000V	Max. Input Current	12*40A	Short Circuit Current	12*50A	AC Output		Rated Output Voltage	3/N/PE 127/220V	Rated Output Current	3*196.8A	Max. Output Current	3*196.8A	Rated Output Frequency	50/60Hz	Rated Output Power	75000W	Max. Apparent Power	75000VA	Power Factor Range	0.8 Leading...0.8 Lagging	Operating Temperature Range: -30°C~60°C		Protective Class: I		Overvoltage Category: II (DC), III (AC)		Ingress protection: IP66		Inverter Topology: Non-isolated		IEC/EN62109-1/2, EN61000-6-2/4	
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GUANGZHOU SANJING ELECTRIC CO., LT

Add: SAJ Innovation Park, No.9, Lizhishan Road, Science City, Guangzhou High-tech Zone, Guangdong, P.R.China

Importer:

Telephone:

Address:

List of Attachments (including a total number of pages in each attachment):

- ATTACHMENT 1 – Photo Documentation (23 pages)

Summary of testing
Tests performed (name of test and test clause):

☒ 4	Utility compatibility
☒ 4.1	Voltage, current and frequency
☒ 4.3	Flicker
☒ 4.4	DC injection
☒ 4.6	Harmonics and waveform distortion
☒ 4.7	Power factor
☒ 5	Personnel safety and equipment protection
☒ 5.2	Over/under voltage and frequency
☒ 5.2.1	Over/under voltage
☒ 5.2.2	Over/under frequency
☒ 5.3	Islanding protection
☒ 5.4	Response to utility recovery

Testing location:

The laboratory described on page 5.

☒ The product fulfils the requirements of IEC 62116: 2014.

Equipment mobility	☐ movable ☐ stationary	☐ hand-held ☒ fixed
Connection to the mains	☐ pluggable equipment ☒ permanent connection	☐ direct plug-in ☐ for building-in
Operating condition	☒ continuous ☐ short-time ☐ intermittent	
Over voltage category	☐ OVC I ☒ OVC III	☒ OVC II ☐ OVC IV
Mains supply tolerance (%)	According to specified supply range	
Tested for IT power systems	☐ Yes	☒ No
IT testing, phase-phase voltage (V)	N/A	
Class of equipment	☒ Class I ☐ Class III	☐ Class II ☐ Not classified
Mass of equipment (kg)	See Table 2	
Pollution degree	☐ PD 1 ☐ PD 2 ☒ PD 3(internal reduced to PD 2)	
IP protection class	IP66	

Possible test case verdicts:

- test case does not apply to the test object..... : N/A
- test object does meet the requirement : Pass (P)
- test object does not meet the requirement : Fail (F)

Testing:

Date of receipt of test items : See cover page

Date(s) of performance of tests : See cover page

General remarks:

"(see Attachment #)" refers to additional information appended to the report.

"(see appended table)" refers to a table appended to the report.

The tests results presented in this report relate only to the object tested.

This report shall not be reproduced except in full without the written approval of the testing laboratory.

List of test equipment must be kept on file and available for review.

Additional test data and/or information provided in the attach2116ments to this report.

Throughout this report a ☐ comma / ☒ point is used as the decimal separator.

Determination of the test results includes consideration of measurement uncertainty from the test equipment and methods.

Manufacturer's Declaration per sub-clause 6.2.5 of IEC 60335-1:

The application for obtaining a CB Test Certificate includes more than one factory location and a declaration from the Manufacturer stating that the sample(s) submitted for evaluation is (are) representative of the products from each factory has been provided :

☐ Yes
☒ Not applicable

When differences exist; they shall be identified in the General product information section.

Name and address of factory (ies) : **Guangzhou Sanjing Electric Co., Ltd.**
No.9, Lizhishan Road, Science City,
Guangzhou Hightech Zone, Guangdong
P.R. China

General product information:

Brief description:

The equipment is a three-phase public interactive photovoltaic inverter, which will be installed and connected to the grid after installation.

It contains filters for smoothing output voltage and EMC, switching and control circuits. Electronic circuits are mounted on multiple PCBs interconnected by appropriate connectors and wires.

The power board including the electronic components is mounted on the radiator and grounded through metal screws and spring washers.

Communication ports include USB, DRM and RS485, which are connected to a monitor to monitor the status of the inverter through proprietary software.

PV input combiner with 6, 9 or 12 MPPT tracers, and each MPPT tracer includes 4 PV input terminals (2PV+ and 2PV-).

The AC output is directly connected to the power grid, and the protective grounding is provided by a dedicated grounding terminal.

The grid-connected protection combines the two series of relays as a redundant construction to ensure that the inverter can independently disconnect from the grid when the relay fails.

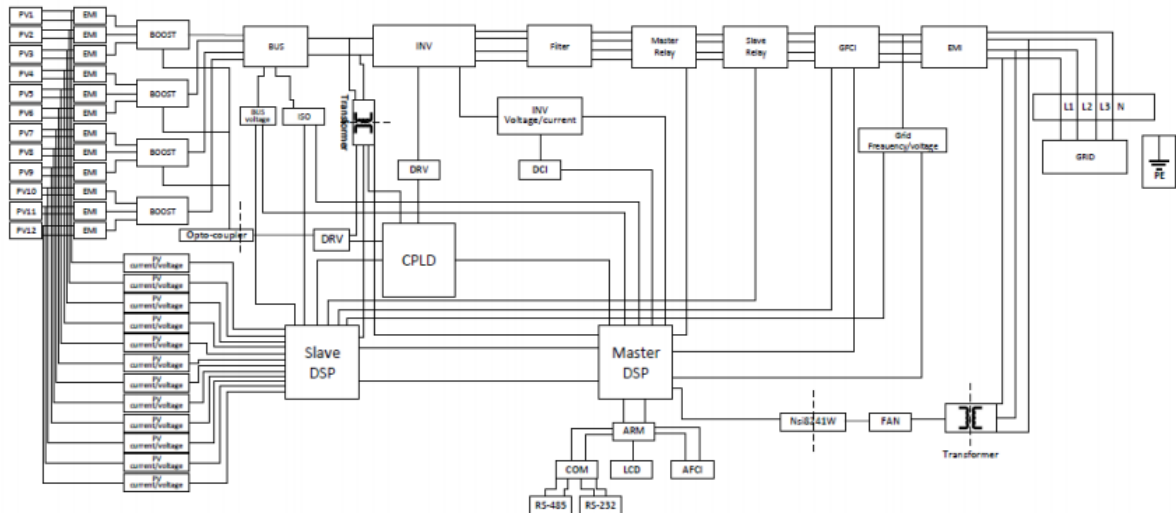
In the case of a fault defined in this standard, after the DSP receives an abnormal signal from the relevant protection detection circuit, the relay will act to automatically disconnect the active circuit of the photovoltaic inverter from the grid, when any grid fault occurs, the master DSP and the slave DSP have the ability to disconnect from the grid independently.

Model difference:

The models C6-50K-T6-LV-40, C6-60K-T9-LV-40, C6-70K-T12-LV-40, and C6-75K-T12-LV-40 are the same in structure and hardware except for the differences listed in the table below. Output power is adjusted by software. Details can be found below:

Model Part	C6-50K-T6-LV-40	C6-60K-T9-LV-40	C6-70K-T12-LV-40	C6-75K-T12-LV-40
Power board version	C6.212100-212136.MPB-I1 V1.0		C6.212100-212136.MPB-V V1.0	
Number of strings	2			
Number of MPPT tracers	6	9	12	12
Max input current	40A			
Short circuit current	50A			
Rated ouputpower (KW)	50	60	70	75

The system diagram block is shown as below:



Block diagram

Description of change(s):

This report is based on the report CN22DK54 001 and modified by adding models C6-50K-T6-LV-40, C6-60K-T9-LV-40, C6-70K-T12-LV-40 and C6-75K-T12-LV-40.

The models C6-50K-T6-LV-40, C6-60K-T9-LV-40, C6-70K-T12-LV-40 and C6-75K-T12-LV-40 are identical to the original models C6-50K-T6-LV, C6-60K-T9-LV and C6-70K-T12-LV in terms of the structure and hardware. The difference is the PCB layout of main power board in order to get a better heat dissipation, the output voltage and output power are different with adjusted by software. The input max current changes from 30A to 40A.

Details can be found below:

Model	C6-50K-T6-LV-40	C6-60K-T9-LV-40	C6-70K-T12-LV-40	C6-75K-T12-LV-40
Max. Input Current IMAX [A]	6*40	9*40	12*40	12*40

History of amendments and modifications:

Report No.: CN22DK54 001, dated 2022-11-30 (original)

Report No.: CN22DK54 002, dated 2025-03-06 (1st Modification)

Model list		C6-50K-T6-LV-40	C6-60K-T9-LV-40	C6-70K-T12-LV-40	C6-75K-T12-LV-40
PV INPUT	VMAX PV [Vdc]	1100			
	ISC PV [A]	6*50	9*50	12*50	12*50
	MPPT Voltage Range VMPP [Vdc]	180-1000			
	Max. Input Current I _{MAX} [A] (A/B) (each MPPT if more than 1)	6*40	9*40	12*40	12*40
	MPPT Full Power Voltage Range [Vdc]	550-850			
	Number of MPPT	6	9	12	12
	String per MPPT	2			
	Backfeed Current [A]	0			
	Overvoltage Category(OVC)	II			
AC Side(ON-Grid)	Rated Output Voltage Ur [Vac]	3L+N+PE, 127/220			
	Rated Output Frequency FNETZ [Hz]	50/60			
	Rated Output Power PE [kW]	50	60	70	75
	Max. Apparent power S _{Emax} [VA]	55	60	70	75
	Rated Output Current Ir [A]	131.2	157.5	183.7	196.8
	Max. Output Current I _{max} [A]	144.3	157.5	183.7	196.8
	Power Factor cosφ [λ]	0.8 leading~0.8 lagging			
	Efficiency max. η _{max}	98.8%			
	Efficiency Euro	98.5%			
	THD [V / I] (100% full power)	<3%			
	Acoustic Noise [dB]	<50			
	Overvoltage Category (OVC)	III			
CONS	Hardware [Version]	Control board:V1.2, IO board:V1.0, Main power board:V1.0, Boost drive board:V1.0,			

		INV drive broad:V1.0 ARM broad:V1.4, PV Input broad:V1.2, AC output broad:V1.4, Electric capacity broad:V1.0, Lightning protection broad:V1.0, Communication broad: V1.1, Display broad:V1.3	
Firmware [Version]		V0.082	
Array Insulation Resistance Detection [Ω]		150K	
Type of inverter		Non-isolated	
Separated by		Transformer less	
Type of NS Protection		Integrated	
Protective Class		Class I	
Enclosure Protection (IP)		IP66	
Operating Temperature Range [$^{\circ}\text{C}$]		-30 $^{\circ}\text{C}$ to +60 $^{\circ}\text{C}$ (45 $^{\circ}\text{C}$ to 60 $^{\circ}\text{C}$ with derating)	
Pollution degree (PD)		PD3	
Altitude [m]		3000	
Size [mm]		660*1045*361	
Weight [kg]		93	98

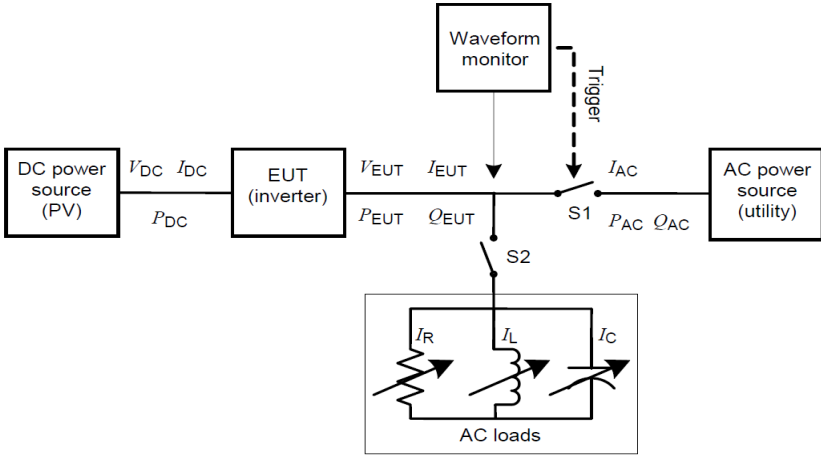
IEC 61727: 2004			
Clause	Requirement – Test	Result - Remark	Verdict
4	Utility compatibility	See below.	P
	The quality of power provided by the PV system for the on-site AC loads and for power delivered to the utility is governed by practices and standards on voltage, flicker, frequency, harmonics and power factor. Deviation from these standards represents out-of-bounds conditions and may require the PV system to sense the deviation and properly disconnect from the utility system.	Compliance.	P
	All power quality parameters (voltage, flicker, frequency, harmonics, and power factor) must be measured at the utility interface/ point of common coupling unless otherwise specified. NOTE Balancing phase currents in multiphase systems is desirable.		P
4.1	Voltage, current and frequency	See below.	P
	The PV system AC voltage, current and frequency shall be compatible with the utility system.	See appended table 4.1.	P
4.2	Normal voltage operating range	See below.	P
	Utility-interconnected PV systems do not normally regulate voltage, they inject current into the utility. Therefore, the voltage operating range for PV inverters is selected as a protection function that responds to abnormal utility conditions, not as a voltage regulation function.	Compliance.	P
4.3	Flicker	See below.	P
	The operation of the PV system should not cause voltage flicker in excess of limits stated in the relevant sections of IEC 61000-3-3 for systems less than 16 A or IEC 61000-3-5 for systems with current of 16 A and above.	See appended table 4.3.	P
4.4	DC injection	See appended table 4.4.	P
	The PV system shall not inject DC current greater than 1% of the rated inverter output current, into the utility AC interface under any operating condition.		P
4.5	Normal frequency operating range	See below.	P
	The PV system shall operate in synchronism with the utility system, and within the frequency trip limits defined in 5.2.2.	Compliance.	P
4.6	Harmonics and waveform distortion	See below.	P
	Low levels of current and voltage harmonics are desirable; the higher harmonic levels increase the potential for adverse effects on connected equipment.	Compliance.	P

IEC 61727: 2004																			
Clause	Requirement – Test	Result - Remark	Verdict																
	Acceptable levels of harmonic voltage and current depend upon distribution system characteristics, type of service, connected loads/apparatus, and established utility practice.	Compliance.	P																
	The PV system output should have low current-distortion levels to ensure that no adverse effects are caused to other equipment connected to the utility system.	Compliance.	P																
	Total harmonic current distortion shall be less than 5% at rated inverter output. Each individual harmonic shall be limited to the percentages listed in Table 1.	See appended table 4.6.	P																
	Even harmonics in these ranges shall be less than 25% of the lower odd harmonic limits listed.	See appended table 4.6.	P																
	Table 1-Current distortion limits <table><tr><th>Odd harmonics</th><th>Distortion limit</th></tr><tr><td>3rd through 9th</td><td>Less than 4.0%</td></tr><tr><td>11th through 15th</td><td>Less than 2.0%</td></tr><tr><td>17th through 21st</td><td>Less than 1.5%</td></tr><tr><td>23rd through 33rd</td><td>Less than 0.6%</td></tr><tr><th>Even harmonics</th><th>Distortion limit</th></tr><tr><td>2rd through 8th</td><td>Less than 1.0%</td></tr><tr><td>10th through 32nd</td><td>Less than 0.5%</td></tr></table> NOTE Testing harmonics is very problematic, since voltage distortion may feed to enhanced current distortion. The harmonic current injection should be exclusive of any harmonic currents due to harmonic voltage distortion present in the utility grid without the PV system connected. Type tested inverters meeting the above requirements should be deemed to comply without further testing.		Odd harmonics	Distortion limit	3 rd through 9 th	Less than 4.0%	11 th through 15 th	Less than 2.0%	17 th through 21 st	Less than 1.5%	23 rd through 33 rd	Less than 0.6%	Even harmonics	Distortion limit	2 rd through 8 th	Less than 1.0%	10 th through 32 nd	Less than 0.5%	P
Odd harmonics	Distortion limit																		
3 rd through 9 th	Less than 4.0%																		
11 th through 15 th	Less than 2.0%																		
17 th through 21 st	Less than 1.5%																		
23 rd through 33 rd	Less than 0.6%																		
Even harmonics	Distortion limit																		
2 rd through 8 th	Less than 1.0%																		
10 th through 32 nd	Less than 0.5%																		
4.7	Power factor	See below.	P																
	The PV system shall have a lagging power factor greater than 0.9 when the output is greater than 50% of the rated inverter output power. NOTE 1 Specially designed systems that provide reactive power compensation may operate outside of this limit with utility approval. NOTE 2 Most PV inverters designed for utility-interconnected service operate close to unity power factor.	See appended table 4.7.	P																
5	Personnel safety and equipment protection	See below.	P																
	This Clause provides information and considerations for the safe and proper operation of the utility-connected PV systems. NOTE 1 The protection function may be provided as an internal or external device in the system. NOTE 2 IEC 60364-5-55 or national or local codes may be applicable.	Installation, operation and maintain manual provided in English.	P																
5.1	Loss of utility voltage	See below.	P																

IEC 61727: 2004			
Clause	Requirement – Test	Result - Remark	Verdict
	To prevent islanding, a utility connected PV system shall cease to energize the utility system from a de-energized distribution line irrespective of connected loads or other generators within specified time limits.	See clause 5.3.	P
	A utility distribution line can become de-energized for several reasons. For example, a substation breaker opening due to fault conditions or the distribution line switched out during maintenance.		N/A
	If inverters (single or multiple) have DC SELV input and have accumulated power below 1 kw then no mechanical disconnect (relay) is required.		N/A
5.2	Over/under voltage and frequency	See below.	P
	Abnormal conditions can arise on the utility system that require a response from the connected photovoltaic system. This response is to ensure the safety of utility maintenance personnel and the general public, as well as to avoid damage to connected equipment, including the photovoltaic system. The abnormal utility conditions of concern are voltage and frequency excursions above or below the values stated in this Clause, and the complete disconnection of the utility, presenting the potential for a distributed resource island.	Compliance.	P
5.2.1	Over/under voltage	See below.	P
	When the interface voltage deviates outside the conditions specified in Table 2, the photovoltaic system shall cease to energize the utility distribution system. This applies to any phase of a multiphase system.	See appended table 5.2.1.	P
	All discussions regarding system voltage refer to the local nominal voltage.	Compliance.	P
	The system shall sense abnormal voltage and respond. The following conditions should be met, with voltages in RMS and measured at the point of utility connection.	Compliance.	P

IEC 61727: 2004															
Clause	Requirement – Test	Result - Remark	Verdict												
	Table 2 – Response to abnormal voltages <table><thead><tr><th>Voltage (at point of utility connection)</th><th>Maximum trip time*</th></tr></thead><tbody><tr><td>$V < 0,5 \times V_{\text{nominal}}$</td><td>0,1 s</td></tr><tr><td>$50 \% \leq V < 85 \%$</td><td>2,0 s</td></tr><tr><td>$85 \% \leq V \leq 110 \%$</td><td>Continuous operation</td></tr><tr><td>$110 \% < V < 135 \%$</td><td>2,0 s</td></tr><tr><td>$135 \% \leq V$</td><td>0,05 s</td></tr></tbody></table> * Trip time refers to the time between the abnormal condition occurring and the inverter ceasing to energize the utility line. The PV system control circuits shall actually remain connected to the utility to allow sensing of utility electrical conditions for use by the "reconnect" feature.	Voltage (at point of utility connection)	Maximum trip time*	$V < 0,5 \times V_{\text{nominal}}$	0,1 s	$50 \% \leq V < 85 \%$	2,0 s	$85 \% \leq V \leq 110 \%$	Continuous operation	$110 \% < V < 135 \%$	2,0 s	$135 \% \leq V$	0,05 s		
Voltage (at point of utility connection)	Maximum trip time*														
$V < 0,5 \times V_{\text{nominal}}$	0,1 s														
$50 \% \leq V < 85 \%$	2,0 s														
$85 \% \leq V \leq 110 \%$	Continuous operation														
$110 \% < V < 135 \%$	2,0 s														
$135 \% \leq V$	0,05 s														
	The purpose of the allowed time delay is to ride through short-term disturbances to avoid excessive nuisance tripping. The unit does not have to cease to energize if the voltage returns to the normal utility continuous operation condition within the specified trip time. NOTE The voltage drop between the inverter terminals and the point of connection with the utility should be taken into consideration.	Compliance.	P												
5.2.2	Over/under frequency	See below.	P												
	When the utility frequency deviates outside the specified conditions the photovoltaic system shall cease to energize the utility line. The unit does not have to cease to energize if the frequency returns to the normal utility continuous operation condition within the specified trip time.	See appended table 5.2.1.	P												
	When the utility frequency is outside the range of $\pm 1\text{Hz}$, the system shall cease to energize the utility line within 0.2 s. The purpose of the allowed range and time delay is to allow continued operation for short-term disturbances and to avoid excessive nuisance tripping in weak-utility system conditions.	Compliance.	P												
5.3	Islanding protection	See below.	P												
	The PV system must cease to energize the utility line within 2s of loss of utility. NOTE The issues of non-islanding inverter are the subject of another standard under consideration.	The test procedure fulfill IEC 62116. See appended table 6.1.	P												
5.4	Response to utility recovery	See below.	P												
	Following an out-of-range utility condition that has caused the photovoltaic system to cease energizing, the photovoltaic system shall not energize the utility line for 20s to 5min after the utility service voltage and frequency have recovered to within the specified ranges. NOTE The energizing delay is dependent on local conditions.	The recovery time has measured after voltage or frequency out of the range.	P												
5.5	Earthing		P												
	The utility interface equipment shall be earthed/	Compliance.	P												

IEC 61727: 2004			
Clause	Requirement – Test	Result - Remark	Verdict
	grounded in accordance with IEC 60364-7-712.		
5.6	Short circuit protection		P
	The photovoltaic system shall have short- circuit protection in accordance with IEC 60364-7-712.	Circuit breakers are installed on both DC and AC sides to protect short circuit.	P
5.7	Isolation and switching		P
	A method of isolation and switching shall be provided in accordance with IEC 60364-7-712.		P

IEC/EN 62116: 2014			
Clause	Requirement – Test	Result - Remark	Verdict
4	Testing circuit	Compliance.	P
	 IEC 1567/08		P

5	Testing equipment	See below.	P
5.1	Measuring instruments	Compliance.	P
	Waveform observation shall be measured by a device with memory function, for example, a storage or digital oscilloscope or a high speed data acquisition system. The waveform measurement/capture device shall be able to record the waveform from the beginning of the islanding test until the EUT ceases to energize the island. For multi-phase EUT, all phases shall be monitored. A waveform monitor designed to detect and calculate the run-on time may be used.		P
	For multi-phase EUT, the test and measurement equipment shall record each phase current and each phase-to-neutral or phase-to-phase voltage, as appropriate, to determine fundamental frequency active and reactive power flow over the duration of the test. A sampling rate of 10 kHz or higher is recommended. The minimum measurement accuracy shall be 1 % or less of rated EUT nominal output voltage and 1 % or less of rated EUT output current. Current, active power, and reactive power measurements through switch S1 used to determine the circuit balance conditions shall report the fundamental (50 Hz or 60 Hz) component.	Compliance.	P
5.2	DC power source	See below.	P
5.2.1	General		P
	A DC power source, such as a PV array simulator, a PV array, or a current and voltage limited DC power supply with series resistance may be used. If the EUT can operate in utility-interconnected mode from a storage battery, a DC power source may be used in lieu of a battery as long as the DC power source shall not be the limiting device as far as the maximum EUT input current is concerned. The DC power source shall provide voltage and current necessary to meet the testing requirements described in Clause 6.		P
5.2.2	PV array simulator	Compliance.	P
	A unit intended to be energized directly from a photovoltaic source shall be energized from a supply that simulates the current-voltage characteristics and time response of a photovoltaic array. The tests shall be conducted at the input voltage defined in Table 2 below, and the current shall be limited to 1,5 times the rated photovoltaic input current, except when specified otherwise by the test requirements.		P
5.2.3	Current and voltage limited DC power supply with series resistance		N/A
	A DC power source used as the EUT input source shall be capable of EUT maximum input		N/A

	power (so as to achieve EUT maximum output power) at minimum and maximum EUT input operating voltage.														
	The power source should provide adjustable current and voltage limit, set to provide the desired short circuit current and open circuit voltage when combined with the series and shunt resistance described below.		N/A												
	A series resistance (and, optionally, a shunt resistance) should be selected to provide a fill factor within the range shown in Table 2.		N/A												
5.2.4	PV array		N/A												
	A PV array used as the EUT input source shall be capable of EUT maximum input power at minimum and maximum EUT input operating voltage (see Table 3). Testing is limited to times when the irradiance varies by no more than 2 % over the duration of the test as measured by a silicon-type pyranometer or reference device. It may be necessary to adjust the array configuration to achieve the input voltage and power levels prescribed in 6.1.		N/A												
5.3	AC power source	Compliance.	P												
	<table><tr><th>Items</th><th>Conditions</th></tr><tr><td>Voltage</td><td>Nominal ± 2,0 %</td></tr><tr><td>Voltage THD</td><td>< 2,5 %</td></tr><tr><td>Frequency</td><td>Nominal ± 0,1 Hz</td></tr><tr><td>Phase angle distance ^a</td><td>120° ± 1,5°</td></tr><tr><td colspan="2">^a Three-phase case only.</td></tr></table>	Items	Conditions	Voltage	Nominal ± 2,0 %	Voltage THD	< 2,5 %	Frequency	Nominal ± 0,1 Hz	Phase angle distance ^a	120° ± 1,5°	^a Three-phase case only.			P
Items	Conditions														
Voltage	Nominal ± 2,0 %														
Voltage THD	< 2,5 %														
Frequency	Nominal ± 0,1 Hz														
Phase angle distance ^a	120° ± 1,5°														
^a Three-phase case only.															
5.4	AC loads	See below.	P												
	On the AC side of the EUT, variable resistance, capacitance, and inductance shall be connected in parallel as loads between the EUT and the AC power source. Other sources of load, such as electronic loads, may be used if it can be shown that the source does not cause results that are different than would be obtained with passive resistors, inductors, and capacitors.	Compliance.	P												
	All AC loads shall be rated for and adjustable to all test conditions. The equations for Q_f are based upon an ideal parallel RLC circuit. For this reason, non-inductive resistors, low loss (high Q_f) inductors, and capacitors with low effective series resistance and effective series inductance shall be utilized in the test circuit. Iron core inductors, if used, shall not exceed a current THD of 2 % when operated at nominal voltage. Load components should be conservatively rated for the voltage and power levels expected. Resistor power ratings should be chosen so as to minimize thermally-induced drift in resistance values during the course of the test.	Compliance.	P												

	Active and reactive power should be calculated (using the measurements provided in Table 1) in each of the R, L and C legs of the load so that these parasitic parameters (and parasitics introduced by variacs or autotransformers) are properly accounted for when calculating Q_f .	Calculated. See appended table for detail.	P
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6	Test for single or multi-phase inverter	See below.	P
6.1	Test procedure	Compliance.	P
	The following test is designed for an EUT consisting of a single or multi-phase inverter ¹ . The test uses an RLC load, resonant at the EUT nominal frequency (50 Hz or 60 Hz) and matched to the EUT output power. For a multi-phase EUT, the load shall be balanced across all phases and the switch S1 as in Figure 1 shall open all phases ² . This test shall be performed with the EUT conditions as in Table 5, where power and voltage values are given as a percent of EUT full output rating.	50/60Hz, three phase EUT.	P
	EUT settings for voltage and frequency trip parameters (magnitude and timing) can affect the measured run-on time. Passing this test verifies that the unit will provide adequate islanding protection for the settings tested as well as for tighter settings (e.g., an EUT that passes the test with frequency trip settings of $\pm 1,5$ Hz of nominal should also trip within the maximum measured run-on time for settings of, say, $\pm 0,5$ Hz.) Conversely, when adjusted to settings outside of those tested, the EUT may experience extended run-on times. Frequency settings of $\pm 1,5$ Hz around nominal frequency and voltage settings of ± 15 % around nominal voltage, for the purposes of this test procedure, should be wide enough to address the majority of utility requirements. Note that as trip settings are widened, more aggressive active antiislanding schemes may be required that could negatively impact power quality.	Voltage and frequency trip parameters are set according to the requirements of IEC 61727: 2004.	P

	<table><tr><th>Condition</th><th>EUT output power, P_{EUT}</th><th>EUT input voltage ^c</th><th>EUT trip settings ^d</th></tr><tr><td>A</td><td>Maximum ^a</td><td>> 75 % of rated input voltage range</td><td>Voltage and frequency trip settings according to National standards and/or local code</td></tr><tr><td>B</td><td>50 % to 66 % of maximum</td><td>50 % of rated input voltage range, ± 10 %</td><td>Voltage and frequency trip settings according to National standards and/or local code</td></tr><tr><td>C</td><td>25 % to 33 % ^b of maximum</td><td>< 20 % of rated input voltage range</td><td>Voltage and frequency trip settings according to National standards and/or local code</td></tr></table> ^a Maximum EUT output power condition should be achieved using the maximum allowable input power. Actual output power may exceed nominal rated output. ^b Or minimum allowable EUT output level if greater than 33 %. ^c Based on EUT rated input operating range. For example, if range is between X volts and Y volts, 75 % of range = $X + 0.75 \times (Y - X)$. Y shall not exceed $0.8 \times$ EUT maximum system voltage (i.e., maximum allowable array open circuit voltage). In any case, the EUT should not be operated outside of its allowable input voltage range. ^d The manufacturer shall specify the applicable standard, code or utility based trip settings with which the unit shall be tested. The manufacturer may also choose more stringent trip settings to demonstrate compatibility with a greater number of utility requirements. The recommended settings shown below should address the majority of utility requirements. <table><tr><th>Parameter</th><th>Magnitude</th><th>Timing s</th></tr><tr><td>Over voltage</td><td>115 % of nominal voltage</td><td>2</td></tr><tr><td>Under voltage</td><td>85 % of nominal voltage</td><td>2</td></tr><tr><td>Over frequency</td><td>1,5 Hz above nominal frequency</td><td>1</td></tr><tr><td>Under frequency</td><td>1,5 Hz below nominal frequency</td><td>1</td></tr></table> If fast over and under voltage and frequency settings are provided, similarly extended values should also be specified by the manufacturer.	Condition	EUT output power, P_{EUT}	EUT input voltage ^c	EUT trip settings ^d	A	Maximum ^a	> 75 % of rated input voltage range	Voltage and frequency trip settings according to National standards and/or local code	B	50 % to 66 % of maximum	50 % of rated input voltage range, ± 10 %	Voltage and frequency trip settings according to National standards and/or local code	C	25 % to 33 % ^b of maximum	< 20 % of rated input voltage range	Voltage and frequency trip settings according to National standards and/or local code	Parameter	Magnitude	Timing s	Over voltage	115 % of nominal voltage	2	Under voltage	85 % of nominal voltage	2	Over frequency	1,5 Hz above nominal frequency	1	Under frequency	1,5 Hz below nominal frequency	1	P
Condition	EUT output power, P_{EUT}	EUT input voltage ^c	EUT trip settings ^d																														
A	Maximum ^a	> 75 % of rated input voltage range	Voltage and frequency trip settings according to National standards and/or local code																														
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Under frequency	1,5 Hz below nominal frequency	1																															
6.2	Pass/fail criteria	See below.	P																														
	An EUT is considered to comply with the requirements for islanding protection when each case of recorded run-on time is less than 2 s or meets the requirements of local codes.	Compliance.	P																														

7	Documentation		P
Annex A (informative)	Islanding as it applies to PV systems		P
A.1	General		P
A.2	Impact of distortion on islanding		P
Annex B (informative)	Test for independent islanding detection device (relay)	Test with inverter.	N/A
B.1	General		N/A
B.2	Testing circuit		N/A
B.3	Testing equipment		N/A
B.3.1	General		N/A
B.3.2	AC input source		N/A
B.4	Testing procedure		N/A
B.5	Documentation		N/A
Annex C (informative)	Gate blocking signal		N/A
C.1	General		N/A
C.2	Gate blocking signal used in photovoltaic systems		N/A
C.3	Monitoring the gate blocking signal		N/A

4.1	TABLE: Voltage, current and frequency								P
Power conditions	I/P rated	PV / DC Input			O/P rated	Grid / AC Output			
	I [A]	U [V]	I [A]	P [kW]	I [A]	U [V]	I [A]	P [kW]	Fre. [Hz]
C6-75K-T12-LV-40									
(10±5)%	--	598.5	13.03	7782.2	196.8	127.1	20.82	2507.8	60.0
						127.1	20.50	2504.4	60.0
						127.0	20.85	2500.7	60.0
(50±5)%	--	600.6	65.58	38943.8	196.8	127.1	104.0	12541.8	60.0
						127.1	102.46	12517.3	60.0
						127.0	104.19	12505.3	60.0
(100±5)%	--	598.78	129.26	77748.7	196.8	127.1	207.87	25046.3	60.0
						127.1	204.58	25020.8	60.0
						127.0	208.50	24951.2	60.0
C6-50K-T6-LV-40									
(10±5)%	--	597.8	8.86	5285.9	144.3	127.2	14.10	1703.0	60.0
						127.1	13.90	1699.9	60.0
						127.1	14.13	1698.0	60.0
(50±5)%	--	596.4	43.51	25899.5	144.3	127.2	69.09	8344.3	60.0
						127.1	68.10	8327.1	60.0
						127.1	69.24	8317.9	60.0
(100±5)%	--	598.4	86.77	51837.2	144.3	127.2	138.42	16697.7	60.0
						127.1	136.41	16666.7	60.0
						127.0	138.77	16648.0	60.0
Note(s): Test on model C6-75K-T12-LV-40 and C6-50K-T6-LV-40 to represent all the other models.									

4.3	TABLE: Flicker					P
For Model C6-75K-T12-LV-40						
PHASE A		Pst	dc(%)	dmax(%)	d(t)	
	Limit	1.000	3.300	4.000	500	
	1	0.117	0.400	0.449	0.0	
	2	0.118	0.402	0.446	0.0	
	3	0.111	0.372	0.416	0.0	
	4	0.088	0.294	0.337	0.0	
	5	0.118	0.402	0.454	0.0	
	6	0.139	0.478	0.524	0.0	
	7	0.087	0.294	0.333	0.0	
	8	0.111	0.371	0.418	0.0	
	9	0.141	0.481	0.525	0.0	
	10	0.117	0.401	0.444	0.0	
	11	0.110	0.370	0.412	0.0	
	12	0.110	0.371	0.420	0.0	
	Plt = 0.116, Limit = 0.65					
PHASE B		Pst	dc(%)	dmax(%)	d(t)	
	Limit	1.000	3.300	4.000	500	
	1	0.118	0.387	0.431	0.0	
	2	0.115	0.386	0.416	0.0	
	3	0.119	0.403	0.438	0.0	
	4	0.094	0.324	0.362	0.0	
	5	0.116	0.392	0.430	0.0	
	6	0.139	0.475	0.540	0.0	
	7	0.099	0.325	0.353	0.0	
	8	0.121	0.404	0.450	0.0	
	9	0.139	0.461	0.500	0.0	
	10	0.121	0.386	0.415	0.0	
	11	0.116	0.400	0.432	0.0	
	12	0.121	0.402	0.441	0.0	
	Plt = 0.119, Limit = 0.65					
PHASE C		Pst	dc(%)	dmax(%)	d(t)	
	Limit	1.000	3.300	4.000	500	
	1	0.118	0.403	0.434	0.0	
	2	0.118	0.403	0.426	0.0	
	3	0.116	0.401	0.427	0.0	

	4	0.097	0.324	0.355	0.0
	5	0.116	0.394	0.425	0.0
	6	0.139	0.470	0.492	0.0
	7	0.094	0.325	0.347	0.0
	8	0.117	0.403	0.431	0.0
	9	0.140	0.479	0.507	0.0
	10	0.119	0.401	0.425	0.0
	11	0.115	0.404	0.426	0.0
	12	0.119	0.401	0.433	0.0
	Plt = 0.119, Limit = 0.65				
For Model C6-50K-T6-LV-40					
PHASE A		Pst	dc(%)	dmax(%)	d(t)
	Limit	1.000	3.300	4.000	500
	1	0.622	0.477	1.433	0.0
	2	0.633	0.477	1.468	0.0
	3	0.624	0.482	1.651	0.0
	4	0.631	0.479	1.687	0.0
	5	0.630	0.479	1.644	0.0
	6	0.628	0.481	1.691	0.0
	7	0.627	0.482	1.664	0.0
	8	0.635	0.481	1.484	0.0
	9	0.625	0.480	1.676	0.0
	10	0.625	0.475	1.620	0.0
	11	0.630	0.475	1.663	0.0
	12	0.633	0.479	1.499	0.0
	Plt = 0.629, Limit = 0.65				
PHASE B		Pst	dc(%)	dmax(%)	d(t)
	Limit	1.000	3.300	4.000	500
	1	0.320	0.463	0.998	0.0
	2	0.323	0.465	1.007	0.0
	3	0.319	0.464	1.004	0.0
	4	0.314	0.466	0.998	0.0
	5	0.317	0.460	1.002	0.0
	6	0.316	0.463	1.004	0.0
	7	0.317	0.461	1.007	0.0
	8	0.320	0.460	1.008	0.0
	9	0.320	0.463	1.024	0.0

	10	0.320	0.463	1.012	0.0
	11	0.320	0.461	0.993	0.0
	12	0.319	0.463	1.006	0.0
	Plt = 0.319, Limit = 0.65				
PHASE C		Pst	dc(%)	dmax(%)	d(t)
	Limit	1.000	3.300	4.000	500
	1	0.528	0.462	1.464	0.0
	2	0.528	0.462	1.461	0.0
	3	0.528	0.463	1.453	0.0
	4	0.530	0.465	1.467	0.0
	5	0.528	0.463	1.459	0.0
	6	0.527	0.462	1.462	0.0
	7	0.528	0.463	1.454	0.0
	8	0.526	0.464	1.447	0.0
	9	0.527	0.464	1.451	0.0
	10	0.526	0.463	1.463	0.0
	11	0.528	0.465	1.437	0.0
	12	0.526	0.463	1.422	0.0
	Plt = 0.528, Limit = 0.65				
	Note(s): Test on model C6-75K-T12-LV-40 and C6-50K-T6-LV-40 to represent all the other models. Maximum permissible voltage fluctuation (expressed as a percentage of nominal voltage at 100% power) and flicker. More than 16 A per phase according to IEC61000-3-11.				

4.4		TABLE: DC Injection					P
For model C6-75K-T12-LV-40							
Power conditions		33%		66%		100%	
Limits		1%					
Measurement		[A]	[%]	[A]	[%]	[A]	[%]
Test value	PHASE A	0.19	0.10	0.23	0.12	0.20	0.10
	PHASE B	0.17	0.09	0.19	0.10	0.16	0.08
	PHASE C	0.01	0.01	0.02	0.01	0.07	0.03
For model C6-50K-T6-LV-40							
Power conditions		33%		66%		100%	
Limits		1%					
Measurement		[A]	[%]	[A]	[%]	[A]	[%]
Test value	PHASE A	0.11	0.08	0.14	0.11	0.24	0.18
	PHASE B	0.15	0.11	0.13	0.10	0.13	0.10
	PHASE C	0.15	0.11	0.18	0.14	0.20	0.15
Note(s): If the inverter does not incorporate a mains frequency isolating transformer, it shall be type tested to ensure the dc output current at the ac terminals of the inverter is below the above limits at all power levels. Test on model C6-75K-T12-LV-40 and C6-50K-T6-LV-40 to represent all the other models.							

4.6	TABLE: Harmonics and waveform distortion (Current)				P
C6-75K-T12-LV-40					
Phase A					
Harmonics	Measurements (At Rated Current) [%]			Limits [%]	Verdict
	33%	66%	100%		
THD (to the 50 th)	4.134	2.786	1.862	5	P
2 nd	0.294	0.551	0.651	1	P
3 rd	0.029	0.034	0.038	4	P
4 th	0.159	0.195	0.137	1	P
5 th	1.018	1.292	1.040	4	P
6 th	0.030	0.020	0.023	1	P
7 th	0.719	1.085	1.085	4	P
8 th	0.043	0.030	0.060	1	P
9 th	0.010	0.036	0.044	4	P
10 th	0.082	0.069	0.074	0.5	P
11 th	0.410	0.872	0.812	2	P
12 th	0.036	0.063	0.060	0.5	P
13 th	0.350	0.893	0.879	2	P
14 th	0.070	0.188	0.173	0.5	P
15 th	0.027	0.069	0.072	2	P
16 th	0.041	0.179	0.156	0.5	P
17 th	0.265	0.864	1.175	1.5	P
18 th	0.051	0.058	0.066	0.5	P
19 th	0.145	0.335	0.537	1.5	P
20 th	0.035	0.062	0.083	0.5	P
21 th	0.022	0.036	0.068	1.5	P
22 th	0.028	0.038	0.055	0.5	P
23 th	0.077	0.157	0.232	0.6	P
24 th	0.013	0.013	0.016	0.5	P
25 th	0.056	0.083	0.119	0.6	P
26 th	0.019	0.021	0.038	0.5	P
27 th	0.010	0.011	0.020	0.6	P
28 th	0.022	0.017	0.028	0.5	P
29 th	0.069	0.057	0.087	0.6	P
30 th	0.009	0.010	0.013	0.5	P
31 th	0.067	0.049	0.085	0.6	P

32 th	0.028	0.013	0.033	0.5	P
33 th	0.006	0.013	0.018	0.6	P
Phase B					
Harmonics	Measurements (At Rated Current) [100%]			Limits [%]	Verdict
	33%	66%	100%		
THD (to the 40 th)	4.123	3.488	2.271	5	P
2 nd	0.291	0.511	0.687	1	P
3 rd	0.041	0.045	0.040	4	P
4 th	0.156	0.197	0.144	1	P
5 th	1.023	1.292	1.035	4	P
6 th	0.016	0.014	0.027	1	P
7 th	0.716	1.092	1.104	4	P
8 th	0.042	0.027	0.064	1	P
9 th	0.020	0.036	0.027	4	P
10 th	0.081	0.070	0.058	0.5	P
11 th	0.384	0.864	0.801	2	P
12 th	0.033	0.050	0.061	0.5	P
13 th	0.327	0.854	0.815	2	P
14 th	0.075	0.192	0.164	0.5	P
15 th	0.023	0.069	0.068	2	P
16 th	0.033	0.184	0.145	0.5	P
17 th	0.278	0.912	1.199	1.5	P
18 th	0.034	0.044	0.060	0.5	P
19 th	0.154	0.349	0.574	1.5	P
20 th	0.036	0.065	0.079	0.5	P
21 th	0.015	0.034	0.057	1.5	P
22 th	0.023	0.041	0.060	0.5	P
23 th	0.077	0.154	0.221	0.6	P
24 th	0.014	0.011	0.011	0.5	P
25 th	0.067	0.096	0.133	0.6	P
26 th	0.018	0.018	0.031	0.5	P
27 th	0.008	0.009	0.012	0.6	P
28 th	0.021	0.017	0.030	0.5	P
29 th	0.069	0.054	0.082	0.6	P
30 th	0.008	0.008	0.010	0.5	P
31 th	0.064	0.046	0.080	0.6	P

32 th	0.028	0.012	0.029	0.5	P
33 th	0.007	0.013	0.014	0.6	P
Phase C					
Harmonics	Measurements (At Rated Current) [100%]			Limits [%]	Verdict
	33%	66%	100%		
THD (to the 40 th)	4.116	3.462	2.045	5	P
2 nd	0.282	0.528	0.605	1	P
3 rd	0.019	0.023	0.024	4	P
4 th	0.149	0.198	0.142	1	P
5 th	1.012	1.276	1.015	4	P
6 th	0.025	0.019	0.031	1	P
7 th	0.732	1.114	1.123	4	P
8 th	0.054	0.030	0.063	1	P
9 th	0.016	0.014	0.035	4	P
10 th	0.085	0.062	0.069	0.5	P
11 th	0.385	0.831	0.779	2	P
12 th	0.034	0.050	0.064	0.5	P
13 th	0.349	0.918	0.906	2	P
14 th	0.085	0.191	0.170	0.5	P
15 th	0.034	0.071	0.073	2	P
16 th	0.042	0.224	0.173	0.5	P
17 th	0.261	0.879	1.240	1.5	P
18 th	0.035	0.051	0.053	0.5	P
19 th	0.146	0.344	0.579	1.5	P
20 th	0.036	0.068	0.090	0.5	P
21 th	0.016	0.034	0.041	1.5	P
22 th	0.028	0.039	0.058	0.5	P
23 th	0.081	0.160	0.223	0.6	P
24 th	0.011	0.014	0.017	0.5	P
25 th	0.060	0.091	0.132	0.6	P
26 th	0.020	0.021	0.038	0.5	P
27 th	0.009	0.014	0.020	0.6	P
28 th	0.020	0.015	0.027	0.5	P
29 th	0.068	0.055	0.081	0.6	P
30 th	0.010	0.010	0.013	0.5	P
31 th	0.066	0.048	0.079	0.6	P

32 th	0.030	0.013	0.027	0.5	P
33 th	0.008	0.010	0.014	0.6	P
C6-50K-T6-LV-40					
Phase A					
Harmonics	Measurements (At Rated Current) [%]			Limits [%]	Verdict
	33%	66%	100%		
THD (to the 50 th)	2.443	3.674	2.125	5	P
2 nd	0.816	0.551	0.651	1	P
3 rd	0.053	0.034	0.038	4	P
4 th	0.626	0.195	0.137	1	P
5 th	0.818	1.292	1.040	4	P
6 th	0.015	0.020	0.023	1	P
7 th	0.384	1.085	1.085	4	P
8 th	0.218	0.030	0.060	1	P
9 th	0.039	0.036	0.044	4	P
10 th	0.080	0.069	0.074	0.5	P
11 th	0.471	0.872	0.812	2	P
12 th	0.027	0.063	0.060	0.5	P
13 th	0.770	0.893	0.879	2	P
14 th	0.049	0.188	0.173	0.5	P
15 th	0.025	0.069	0.072	2	P
16 th	0.067	0.179	0.156	0.5	P
17 th	0.827	0.864	1.175	1.5	P
18 th	0.026	0.058	0.066	0.5	P
19 th	0.589	0.335	0.537	1.5	P
20 th	0.061	0.062	0.083	0.5	P
21 th	0.021	0.036	0.068	1.5	P
22 th	0.045	0.038	0.055	0.5	P
23 th	0.315	0.157	0.232	0.6	P
24 th	0.011	0.013	0.016	0.5	P
25 th	0.216	0.083	0.119	0.6	P
26 th	0.037	0.021	0.038	0.5	P
27 th	0.008	0.011	0.020	0.6	P
28 th	0.026	0.017	0.028	0.5	P
29 th	0.185	0.057	0.087	0.6	P
30 th	0.008	0.010	0.013	0.5	P

31 th	0.152	0.049	0.085	0.6	P
32 th	0.018	0.013	0.033	0.5	P
33 th	0.010	0.013	0.018	0.6	P
Phase B					
Harmonics	Measurements (At Rated Current) [100%]			Limits [%]	Verdict
	33%	66%	100%		
THD (to the 40 th)	2.443	3.666	2.131	5	P
2 nd	0.815	0.511	0.687	1	P
3 rd	0.049	0.045	0.040	4	P
4 th	0.601	0.197	0.144	1	P
5 th	0.772	1.292	1.035	4	P
6 th	0.018	0.014	0.027	1	P
7 th	0.358	1.092	1.104	4	P
8 th	0.212	0.027	0.064	1	P
9 th	0.027	0.036	0.027	4	P
10 th	0.086	0.070	0.058	0.5	P
11 th	0.487	0.864	0.801	2	P
12 th	0.025	0.050	0.061	0.5	P
13 th	0.789	0.854	0.815	2	P
14 th	0.051	0.192	0.164	0.5	P
15 th	0.022	0.069	0.068	2	P
16 th	0.041	0.184	0.145	0.5	P
17 th	0.826	0.912	1.199	1.5	P
18 th	0.022	0.044	0.060	0.5	P
19 th	0.575	0.349	0.574	1.5	P
20 th	0.044	0.065	0.079	0.5	P
21 th	0.021	0.034	0.057	1.5	P
22 th	0.050	0.041	0.060	0.5	P
23 th	0.326	0.154	0.221	0.6	P
24 th	0.011	0.011	0.011	0.5	P
25 th	0.243	0.096	0.133	0.6	P
26 th	0.039	0.018	0.031	0.5	P
27 th	0.006	0.009	0.012	0.6	P
28 th	0.027	0.017	0.030	0.5	P
29 th	0.184	0.054	0.082	0.6	P
30 th	0.007	0.008	0.010	0.5	P

31 th	0.133	0.046	0.080	0.6	P
32 th	0.016	0.012	0.029	0.5	P
33 th	0.008	0.013	0.014	0.6	P
Phase C					
Harmonics	Measurements (At Rated Current) [100%]			Limits [%]	Verdict
	33%	66%	100%		
THD (to the 40 th)	2.439	3.673	2.122	5	P
2 nd	0.824	0.528	0.605	1	P
3 rd	0.025	0.023	0.024	4	P
4 th	0.643	0.198	0.142	1	P
5 th	0.792	1.276	1.015	4	P
6 th	0.014	0.019	0.031	1	P
7 th	0.350	1.114	1.123	4	P
8 th	0.206	0.030	0.063	1	P
9 th	0.024	0.014	0.035	4	P
10 th	0.085	0.062	0.069	0.5	P
11 th	0.464	0.831	0.779	2	P
12 th	0.023	0.050	0.064	0.5	P
13 th	0.803	0.918	0.906	2	P
14 th	0.033	0.191	0.170	0.5	P
15 th	0.016	0.071	0.073	2	P
16 th	0.039	0.224	0.173	0.5	P
17 th	0.811	0.879	1.240	1.5	P
18 th	0.021	0.051	0.053	0.5	P
19 th	0.582	0.344	0.579	1.5	P
20 th	0.048	0.068	0.090	0.5	P
21 th	0.012	0.034	0.041	1.5	P
22 th	0.044	0.039	0.058	0.5	P
23 th	0.335	0.160	0.223	0.6	P
24 th	0.010	0.014	0.017	0.5	P
25 th	0.219	0.091	0.132	0.6	P
26 th	0.034	0.021	0.038	0.5	P
27 th	0.008	0.014	0.020	0.6	P
28 th	0.023	0.015	0.027	0.5	P
29 th	0.178	0.055	0.081	0.6	P
30 th	0.007	0.010	0.013	0.5	P

31 th	0.140	0.048	0.079	0.6	P
32 th	0.013	0.013	0.027	0.5	P
33 th	0.005	0.010	0.014	0.6	P
Note(s): Test on C6-75K-T12-LV-40 and C6-50K-T6-LV-40 to represent all the other models.					

4.7		TABLE: Power Factor					P
C6-75K-T12-LV-40							
Power conditions		(50±5) %	(60±5) %	(70±5) %	(80±5) %	(90±5) %	(100±5) %
Limits		0.9 (leading / lagging)					
U [V]	L1-N	127.21	127.20	127.20	127.22	127.21	127.20
U [V]	L2-N	127.05	127.07	126.97	127.03	127.02	127.04
U [V]	L3-N	127.00	127.10	127.11	127.13	127.13	127.10
I [A]	L1-N	98.46	118.28	137.96	157.65	177.34	196.88
I [A]	L2-N	98.36	118.13	137.77	157.41	177.06	196.55
I [A]	L3-N	98.68	118.53	138.24	157.94	177.64	197.18
Fre. [Hz]		60.00	60.00	60.00	60.00	60.00	60.00
P [W]		37496.93	45052.21	52548.83	60051.11	67556.40	75007.69
Q [VA]		221.84	166.15	102.17	47.32	-16.97	-83.77
S [VA]		37497.6	45052.5	52548.9	60051.2	67556.5	75007.7
PF [λ]		1.00	1.00	1.00	1.00	1.00	1.00
C6-50K-T6-LV-40							
Power conditions		(50±5) %	(60±5) %	(70±5) %	(80±5) %	(90±5) %	(100±5) %
Limits		0.9 (leading / lagging)					
U [V]	L1-N	127.09	127.05	127.10	127.15	127.12	127.16
U [V]	L2-N	127.09	127.05	127.18	127.15	127.15	127.15
U [V]	L3-N	127.13	127.08	127.15	127.15	127.19	127.10
I [A]	L1-N	65.16	78.42	91.50	104.43	117.52	130.54
I [A]	L2-N	65.73	79.02	92.11	105.06	118.17	131.17
I [A]	L3-N	66.00	79.34	92.46	105.49	118.65	131.74
Fre. [Hz]		60.00	60.00	60.00	60.00	60.00	60.00
P [W]		24977.45	30035.52	35049.23	40000.15	45002.96	49971.01
Q [VA]		329.21	450.81	363.90	336.55	451.24	340.62
S [VA]		24980.10	30039.72	35051.80	40002.64	45006.45	49973.98
PF [λ]		1.00	1.00	1.00	1.00	1.00	1.00
Note(s): Test on model C6-75K-T12-LV-40 and C6-50K-T6-LV-40 to represent all the other models.							

5.2.1	TABLE: Over / Under Voltage						P
Rated voltage [Vrms]:			230V		Voltage accuracy [ΔV]:		1%Un
Frequency [Hz]			50		Rated recover time [s]:		20-300
Power condition:			☒ 100%		☐ (50-66)%		☐ (25-33)%
Setting value U_N [Vrms]	Threshold / Trip value U [Vrms]						Limits
	Measurements						
	M1	M2	M3				
L1-N: 63.5	62.83	63.46	63.50	U < 50%U _o			
L2-N: 63.5	63.45	63.32	63.18				
L3-N: 63.5	63.39	63.41	63.44				
L1-N: 107.95	107.44	107.43	107.35	50%U _o ≤U < 85%U _o			
L2-N: 107.95	107.55	107.41	107.40				
L3-N: 107.95	107.72	107.81	107.73				
L1-N: 139.7	140.38	140.14	140.22	110%U _o < U < 135%U _o			
L2-N: 139.7	140.03	139.99	140.03				
L3-N: 139.7	140.34	140.36	140.37				
L1-N: 171.5	171.62	172.02	171.99	135%U _o ≤ U			
L2-N: 171.5	172.05	172.03	172.05				
L3-N: 171.5	171.82	171.67	171.52				
Note(s): Increasing / Decreasing value ramp of 0.5V. (Voltage veracity < 0.1V).							
Trip $U_o \rightarrow U$	Trip time [ms]					Recover time [s]	
	Measurements				Limits	Measurement	Limits
	Phases	M1	M2	M3			
U<50%U _o	L1-N:	66.0	66.0	63.2	100	102.8	20≤t≤300
	L2-N:	53.6	53.6	52.8		102.6	
	L3-N:	60.8	60.8	60.0		101.0	
50%U _o ≤U < 85%U _o	L1-N:	870.0	866.0	862.0	2000	101.2	20≤t≤300
	L2-N:	854.0	850.0	854.0		101.8	
	L3-N:	858.0	862.0	860.0		102.0	
110%U _o < U < 135%U _o	L1-N:	848.0	844.0	846.0	2000	102.6	20≤t≤300
	L2-N:	854.0	846.0	854.0		102.0	
	L3-N:	840.0	844.0	844.0		102.2	
135%U _o ≤ U	L1-N:	31.4	32.6	33.6	50	102.6	20≤t≤300
	L2-N:	19.0	18.6	22.8		102.2	
	L3-N:	27.6	23.4	28.0		102.6	
Note(s): Test on model C6-75K-T12-LV-40 to represent all the other models.							

5.2.2	TABLE: Over / Under Frequency				P	
Rated voltage [Vrms]:		127		Frequency accuracy [ΔF]:	0.05Hz	
Frequency [Hz]		60		Rated recover time [s]:	20-300	
Power condition:		☒ 100%		☐ (50-66)%	☐ (25-33)%	
Setting value F _N [Hz]	Threshold / Trip value F [Hz]					
	Measurements				Limits	
	M1	M2	M3			
59.00	59.03	59.04	59.04		59.0	
61.00	60.96	60.97	60.95		61.0	
Note(s): Increasing / Decreasing value ramp of 0.01Hz, (Frequency veracity < 10 mHz).						
Trip F _O → F	Trip time [ms]				Recover time [s]	
	Measurements			Limits	Measurement	Limits
	M1	M2	M3			
F _N + ΔF → F _N - ΔF (F<59.0)	71.2	86.0	65.6	200	102.6	20≤t≤300
F _N - ΔF → F _N + ΔF (F>61.0)	99.2	100.4	97.6	200	102.6	20≤t≤300
Note(s): Test on model C6-75K-T12-LV-40 to represent all the other models.						

IEC/EN62116: 2014									
5.3	TABLE: Islanding Protection								
Power 100%									
Conditions	P _w [kW]		Q _L [kVA]		Q _c [kVA]		Q _f	Trip time [ms]	Limitation [ms]
PR: 90% PQ: 110%	L1:	22.87	L1:	23.08	L1:	20.53	0.926	335	2000
	L2:	22.98	L2:	24.03	L2:	21.52	0.970		2000
	L3:	22.05	L3:	23.98	L3:	21.48	0.966		2000
PR: 90% PQ: 105%	L1:	21.82	L1:	22.08	L1:	20.80	0.954	456	2000
	L2:	21.96	L2:	23.05	L2:	21.76	0.996		2000
	L3:	21.12	L3:	22.95	L3:	21.70	0.991		2000
PR: 90% PQ: 100%	L1:	22.92	L1:	23.22	L1:	23.22	0.984	764	2000
	L2:	23.07	L2:	24.21	L2:	24.21	1.029		2000
	L3:	22.12	L3:	24.13	L3:	24.10	1.023		2000
PR: 90% PQ: 95%	L1:	22.92	L1:	23.31	L1:	24.63	1.016	474	2000
	L2:	23.08	L2:	24.30	L2:	25.57	1.057		2000
	L3:	22.12	L3:	24.22	L3:	25.48	1.054		2000
PR: 90% PQ: 90%	L1:	22.99	L1:	23.38	L1:	25.92	1.041	317	2000
	L2:	23.16	L2:	24.37	L2:	26.92	1.084		2000
	L3:	22.18	L3:	24.28	L3:	26.83	1.079		2000
PR: 95% PQ: 110%	L1:	23.07	L1:	23.07	L1:	20.71	0.923	860	2000
	L2:	23.20	L2:	24.04	L2:	21.51	0.958		2000
	L3:	22.17	L3:	23.94	L3:	21.48	0.959		2000
PR: 95% PQ: 90%	L1:	23.11	L1:	23.34	L1:	25.69	1.031	596	2000
	L2:	23.20	L2:	24.36	L2:	26.88	1.080		2000
	L3:	22.23	L3:	24.28	L3:	26.82	1.075		2000
PR: 100% PQ: 110%	L1:	23.86	L1:	21.87	L1:	19.35	0.838	1328	2000
	L2:	24.40	L2:	22.92	L2:	20.44	0.871		2000
	L3:	23.31	L3:	22.78	L3:	20.28	0.867		2000
PR: 95% PQ: 105%	L1:	23.05	L1:	23.11	L1:	21.85	0.948	1128	2000
	L2:	23.28	L2:	24.13	L2:	22.89	0.989		2000
	L3:	22.24	L3:	24.03	L3:	22.74	0.984		2000
PR: 95% PQ: 100%	L1:	23.08	L1:	23.22	L1:	23.20	0.978	1360	2000
	L2:	23.28	L2:	24.19	L2:	24.21	1.017		2000
	L3:	22.27	L3:	24.12	L3:	24.12	1.106		2000
PR: 95% PQ: 95%	L1:	23.28	L1:	23.28	L1:	24.60	1.001	1064	2000
	L2:	23.41	L2:	24.27	L2:	25.56	1.043		2000

	L3:	22.36	L3:	24.18	L3:	25.51	1.040		2000
PR: 100% PQ: 105%	L1:	21.49	L1:	22.05	L1:	20.77	0.965	1512	2000
	L2:	22.57	L2:	23.02	L2:	21.76	0.970		2000
	L3:	21.48	L3:	22.93	L3:	21.69	0.972		2000
PR: 100% PQ: 100%	L1:	23.25	L1:	23.25	L1:	23.22	0.977	1804	2000
	L2:	23.86	L2:	23.89	L2:	23.89	0.987		2000
	L3:	23.01	L3:	24.09	L3:	24.06	0.988		2000
PR: 100% PQ: 95%	L1:	23.26	L1:	22.17	L1:	23.43	0.955	1604	2000
	L2:	23.25	L2:	23.26	L2:	24.52	1.003		2000
	L3:	22.83	L3:	22.99	L3:	24.28	0.972		2000
PR: 105% PQ: 105%	L1:	25.38	L1:	21.87	L1:	20.59	0.818	874	2000
	L2:	25.50	L2:	22.96	L2:	21.73	0.859		2000
	L3:	24.67	L3:	22.77	L3:	21.49	0.846		2000
PR: 105% PQ: 100%	L1:	25.48	L1:	21.93	L1:	21.94	0.843	1280	2000
	L2:	25.60	L2:	23.07	L2:	23.02	0.883		2000
	L3:	24.76	L3:	22.86	L3:	22.84	0.871		2000
PR: 105% PQ: 95%	L1:	25.66	L1:	22.02	L1:	23.29	0.862	866	2000
	L2:	25.68	L2:	23.11	L2:	24.34	0.906		2000
	L3:	24.72	L3:	22.93	L3:	24.19	0.899		2000
PR: 100% PQ: 90%	L1:	23.49	L1:	22.21	L1:	24.72	0.973	1312	2000
	L2:	23.53	L2:	23.32	L2:	25.84	1.020		2000
	L3:	22.89	L3:	23.07	L3:	25.59	0.996		2000
PR: 105% PQ: 110%	L1:	25.51	L1:	21.76	L1:	19.27	0.786	662	2000
	L2:	25.54	L2:	22.86	L2:	20.35	0.828		2000
	L3:	24.67	L3:	22.71	L3:	20.16	0.818		2000
PR: 105% PQ: 90%	L1:	25.74	L1:	22.08	L1:	24.61	0.887	588	2000
	L2:	25.78	L2:	23.22	L2:	25.72	0.930		2000
	L3:	24.78	L3:	22.99	L3:	25.53	0.923		2000
PR: 110% PQ: 110%	L1:	26.70	L1:	22.80	L1:	20.31	0.788	438	2000
	L2:	26.97	L2:	23.68	L2:	21.13	0.815		2000
	L3:	26.01	L3:	23.79	L3:	21.30	0.817		2000
PR: 110% PQ: 105%	L1:	26.79	L1:	22.87	L1:	21.58	0.811	528	2000
	L2:	26.95	L2:	23.80	L2:	22.56	0.845		2000
	L3:	25.89	L3:	23.85	L3:	22.56	0.845		2000
PR: 110% PQ: 100%	L1:	26.88	L1:	22.96	L1:	22.98	0.836	860	2000
	L2:	26.97	L2:	23.86	L2:	23.88	0.871		2000

	L3:	25.92	L3:	23.94	L3:	23.97	0.872		2000
PR: 110% PQ: 95%	L1:	26.97	L1:	21.93	L1:	23.22	0.820	520	2000
	L2:	27.06	L2:	23.10	L2:	24.36	0.860		2000
	L3:	26.07	L3:	22.87	L3:	24.12	0.854		2000
PR: 110% PQ: 90%	L1:	26.86	L1:	22.02	L1:	24.55	0.848	343	2000
	L2:	26.97	L2:	23.16	L2:	25.63	0.887		2000
	L3:	25.96	L3:	22.95	L3:	25.51	0.882		2000
Power 66%									
Conditions	P _w [kW]		Q _L [kVA]		Q _c [kVA]		Q _f	Trip time [ms]	Limitation [ms]
PR: 100% PQ: 95%	L1:	16.02	L1:	16.56	L1:	17.38	1.028	608	2000
	L2:	16.09	L2:	16.53	L2:	17.35	1.026		2000
	L3:	15.55	L3:	16.51	L3:	17.37	1.027		2000
PR: 100% PQ: 96%	L1:	16.00	L1:	16.54	L1:	17.17	1.023	752	2000
	L2:	16.54	L2:	16.53	L2:	17.17	1.022		2000
	L3:	15.55	L3:	16.53	L3:	17.19	1.022		2000
PR: 100% PQ: 97%	L1:	15.99	L1:	16.53	L1:	17.02	1.016	836	2000
	L2:	16.08	L2:	16.53	L2:	17.02	1.016		2000
	L3:	15.55	L3:	16.53	L3:	17.02	1.017		2000
PR: 100% PQ: 98%	L1:	15.99	L1:	16.53	L1:	16.80	1.013	1000	2000
	L2:	16.09	L2:	16.50	L2:	16.83	1.007		2000
	L3:	15.54	L3:	16.51	L3:	16.84	1.011		2000
PR: 100% PQ: 99%	L1:	15.99	L1:	16.53	L1:	16.69	1.006	1112	2000
	L2:	16.09	L2:	16.50	L2:	16.66	1.002		2000
	L3:	15.54	L3:	16.50	L3:	16.66	1.005		2000
PR: 100% PQ: 100%	L1:	15.99	L1:	16.51	L1:	16.50	1.000	1224	2000
	L2:	16.12	L2:	16.48	L2:	16.48	0.999		2000
	L3:	15.52	L3:	16.51	L3:	16.50	1.000		2000
PR: 100% PQ: 101%	L1:	15.97	L1:	16.50	L1:	16.33	0.995	1200	2000
	L2:	16.12	L2:	16.48	L2:	16.32	0.993		2000
	L3:	15.54	L3:	16.47	L3:	16.30	0.994		2000
PR: 100% PQ: 102%	L1:	15.96	L1:	16.48	L1:	16.15	0.990	1100	2000
	L2:	16.11	L2:	16.47	L2:	16.14	0.987		2000
	L3:	15.54	L3:	16.48	L3:	16.17	0.989		2000
PR: 100% PQ: 103%	L1:	15.94	L1:	16.48	L1:	15.99	0.984	932	2000
	L2:	16.11	L2:	16.45	L2:	15.96	0.980		2000
	L3:	15.54	L3:	16.48	L3:	15.97	0.981		2000

PR: 100% PQ: 104%	L1:	15.94	L1:	16.48	L1:	15.82	0.981	792	2000
	L2:	16.02	L2:	16.45	L2:	15.79	0.979		2000
	L3:	15.52	L3:	16.47	L3:	15.81	0.977		2000
PR: 100% PQ: 105%	L1:	15.94	L1:	16.47	L1:	15.64	0.974	636	2000
	L2:	16.05	L2:	16.45	L2:	15.63	0.972		2000
	L3:	15.51	L3:	16.44	L3:	15.61	0.969		2000
Power 33%									
Conditions	P _w [W]		Q _L [VA]		Q _c [VA]		Q _f	Trip time [ms]	Limitation [ms]
PR: 100% PQ: 95%	L1:	8.26	L1:	8.25	L1:	8.67	1.001	574	2000
	L2:	8.19	L2:	8.17	L2:	8.59	1.005		2000
	L3:	8.25	L3:	8.23	L3:	8.65	1.003		2000
PR: 100% PQ: 96%	L1:	8.26	L1:	8.32	L1:	8.65	1.007	804	2000
	L2:	8.19	L2:	8.20	L2:	8.53	1.002		2000
	L3:	8.23	L3:	8.25	L3:	8.56	1.001		2000
PR: 100% PQ: 97%	L1:	8.26	L1:	8.32	L1:	8.58	1.001	864	2000
	L2:	8.19	L2:	8.25	L2:	8.50	1.003		2000
	L3:	8.23	L3:	8.28	L3:	8.53	0.999		2000
PR: 100% PQ: 98%	L1:	8.28	L1:	8.35	L1:	8.55	1.001	1104	2000
	L2:	8.19	L2:	8.29	L2:	8.45	1.003		2000
	L3:	8.23	L3:	8.32	L3:	8.49	0.999		2000
PR: 100% PQ: 99%	L1:	8.28	L1:	8.46	L1:	8.55	1.005	1150	2000
	L2:	8.19	L2:	8.37	L2:	8.46	1.007		2000
	L3:	8.23	L3:	8.41	L3:	8.50	1.007		2000
PR: 100% PQ: 100%	L1:	8.26	L1:	8.46	L1:	8.46	1.002	1380	2000
	L2:	8.19	L2:	8.37	L2:	8.37	1.000		2000
	L3:	8.23	L3:	8.43	L3:	8.43	1.004		2000
PR: 100% PQ: 101%	L1:	8.28	L1:	8.52	L1:	8.43	1.000	1.276	2000
	L2:	8.19	L2:	8.43	L2:	8.34	1.004		2000
	L3:	8.23	L3:	8.47	L3:	8.38	1.002		2000
PR: 100% PQ: 102%	L1:	8.26	L1:	8.56	L1:	8.40	1.004	1136	2000
	L2:	8.19	L2:	8.47	L2:	8.31	1.004		2000
	L3:	8.23	L3:	8.53	L3:	8.37	1.004		2000
PR: 100% PQ: 103%	L1:	8.28	L1:	8.61	L1:	8.35	1.004	944	2000
	L2:	8.19	L2:	8.53	L2:	8.28	1.006		2000
	L3:	8.23	L3:	8.55	L3:	8.29	1.001		2000
PR: 100%	L1:	8.26	L1:	8.64	L1:	8.31	1.003	852	2000

PQ: 104%	L2:	8.19	L2:	8.56	L2:	8.23	1.005		2000
	L3:	8.23	L3:	8.59	L3:	8.26	1.002		2000
PR: 100% PQ: 105%	L1:	8.26	L1:	8.68	L1:	8.26	1.001	800	2000
	L2:	8.19	L2:	8.61	L2:	8.19	1.003		2000
	L3:	8.23	L3:	8.64	L3:	8.22	1.001		2000
Note(s): Test on model C6-75K-T12-LV-40 to represent all the other models.									

End of test report

PHOTO DOCUMENTATION

CN22DK54 002 attachment 1

for

Grid-connected PV Inverter

C6-50K-T6-LV-40, C6-60K-T9-LV-40,
C6-70K-T12-LV-40, C6-75K-T12-LV-40

Guangzhou Sanjing Electric Co., Ltd.



This documentation consists of 23 pages (excluding this cover page)



Figure 1. Front view

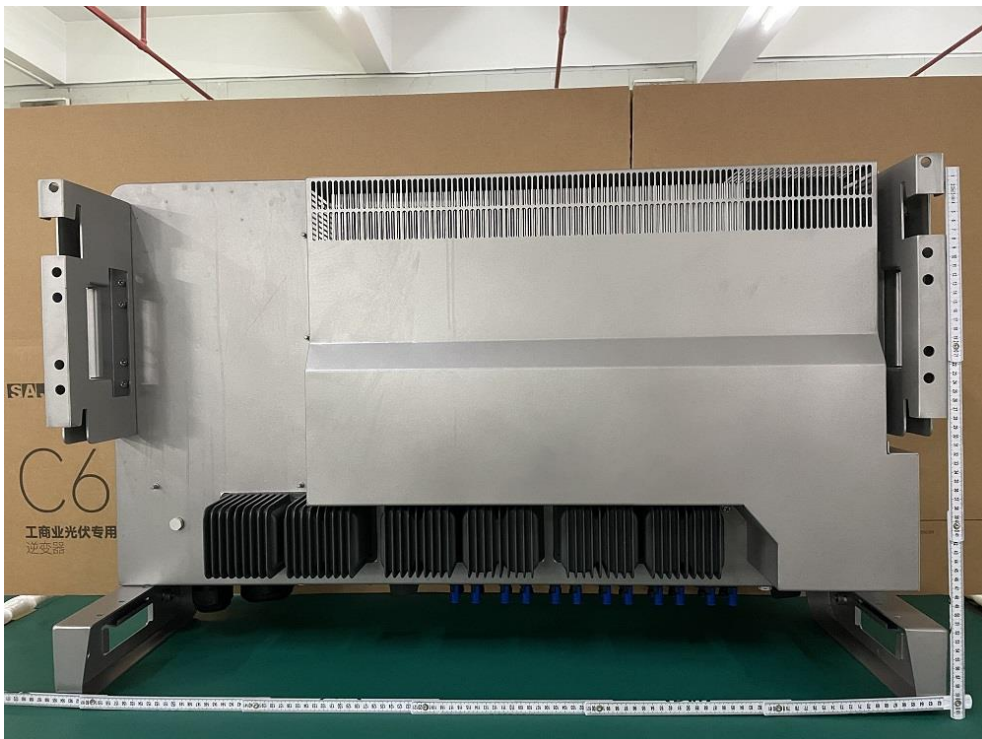


Figure 2. Back view

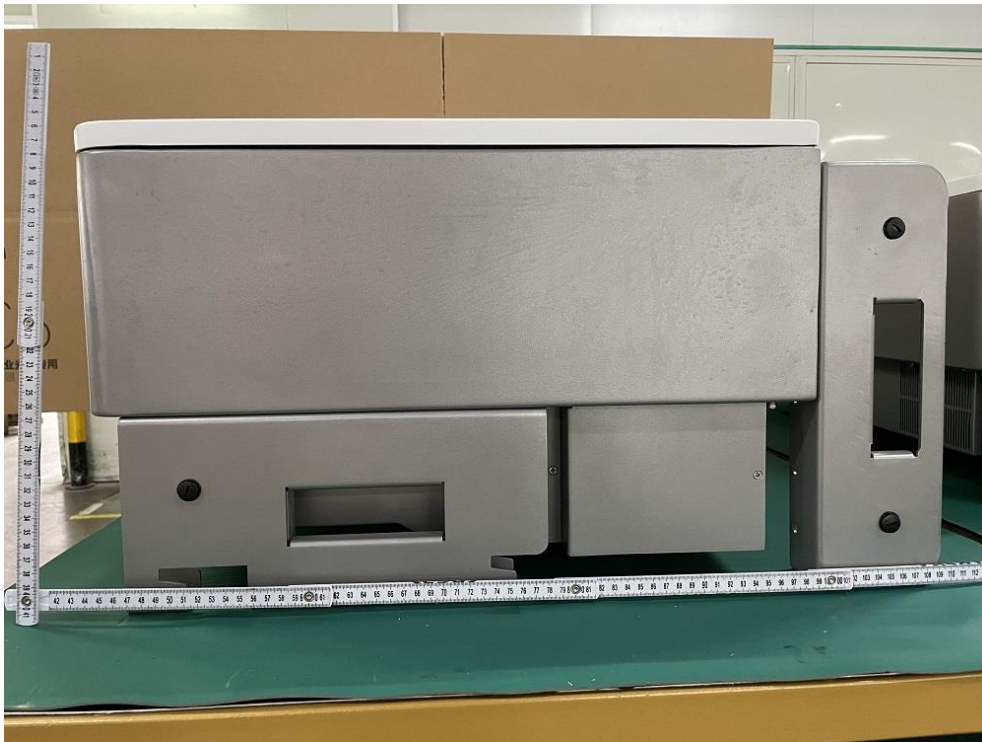


Figure 3. Left view

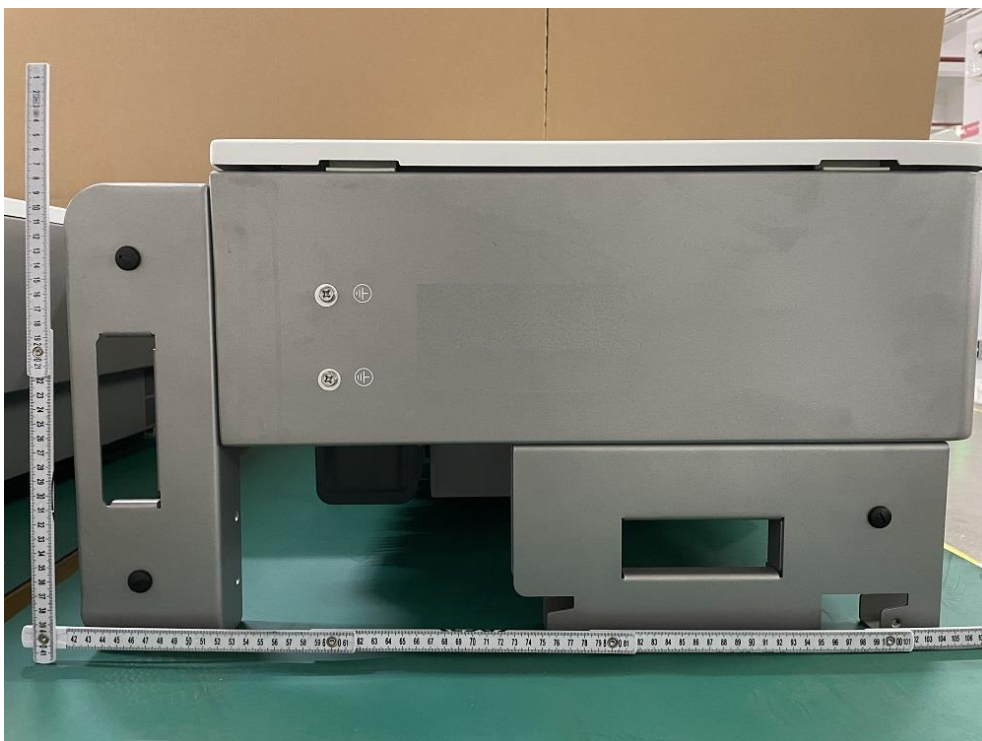


Figure 4. Right view



Figure 5. Top view



Figure 6. Terminal view C6-50K-T6-LV-40



Figure 7. Terminal view of C6-60K-T9-LV-40



Figure 8. Terminal view of C6-70K-T12-LV-40

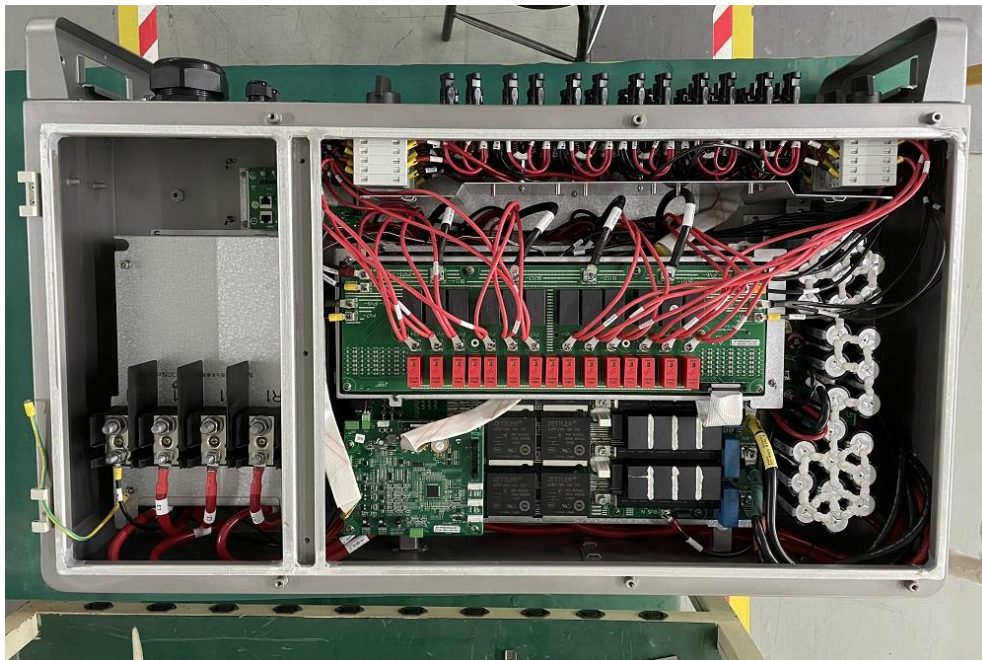


Figure 9. Terminal view 1 of C6-70K-T12-LV-40



Figure 10. Internal view 2 of C6-70K-T12-LV-40

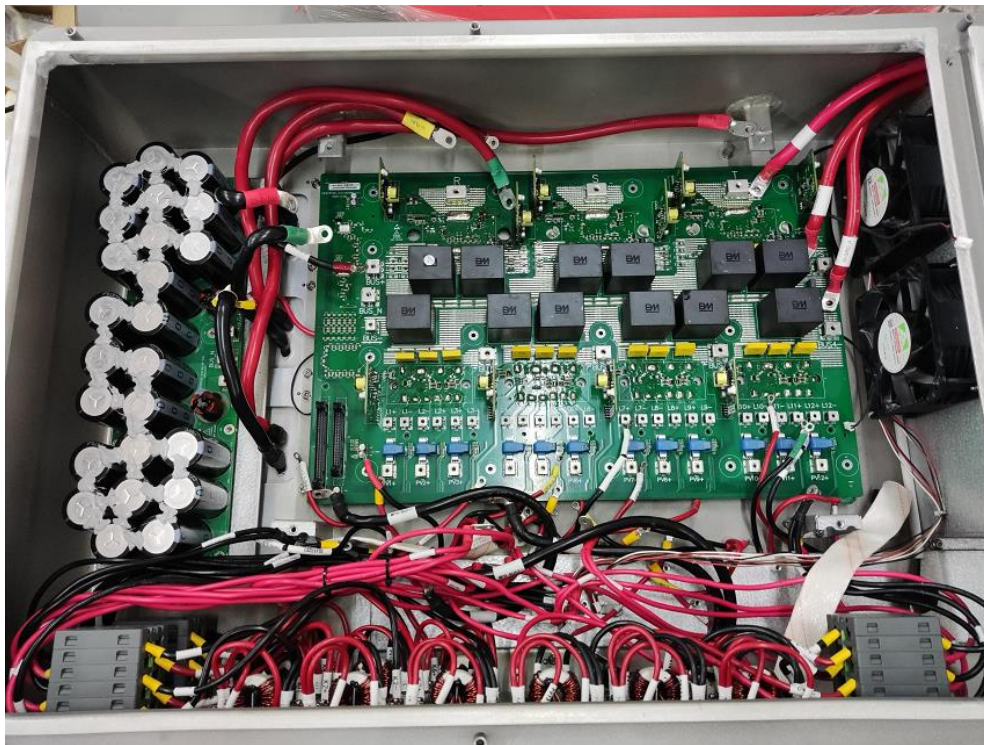


Figure 11. Internal view 3 of C6-70K-T12-LV-40



Figure 12. Internal view 1 of C6-50K-T6-LV-40, C6-60K-T9-LV-40

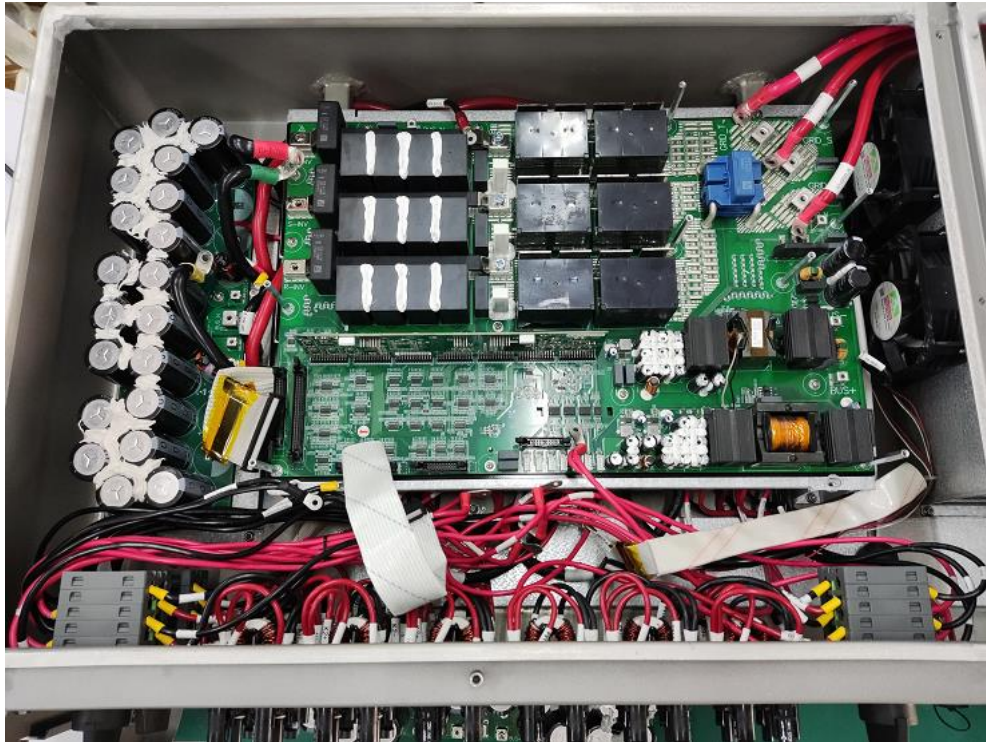


Figure 13. Internal view 2 of C6-50K-T6-LV-40, C6-60K-T9-LV-40



Figure 14. Internal view 3 of C6-50K-T6-LV-40, C6-60K-T9-LV-40

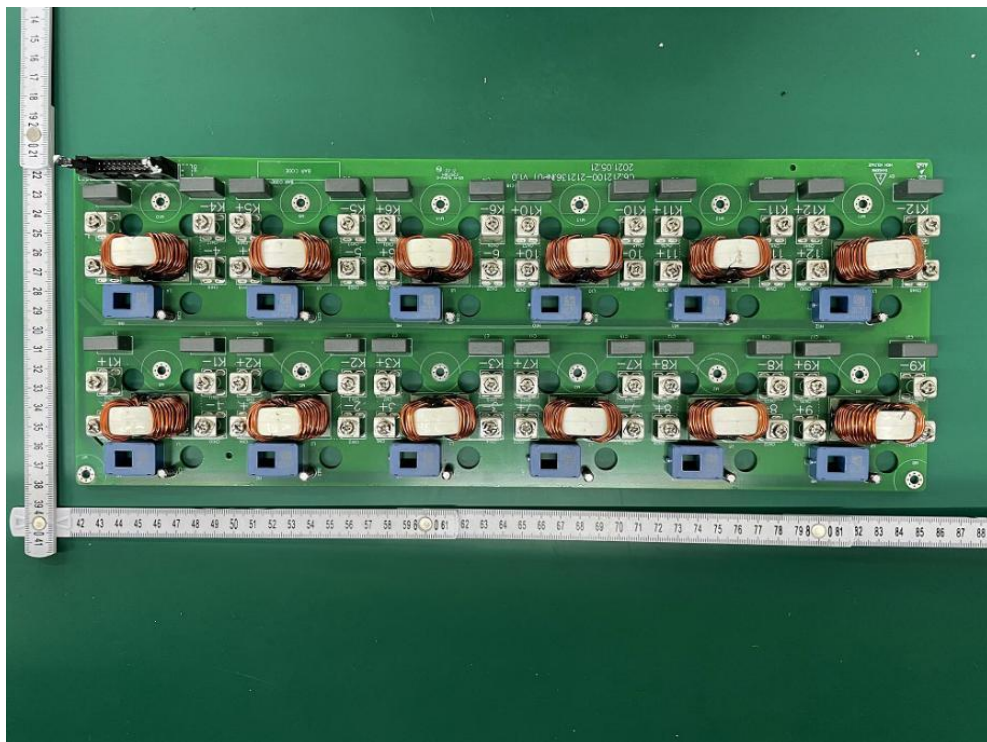


Figure 15. Front view of PV input board for C6-70K-T12-LV-40

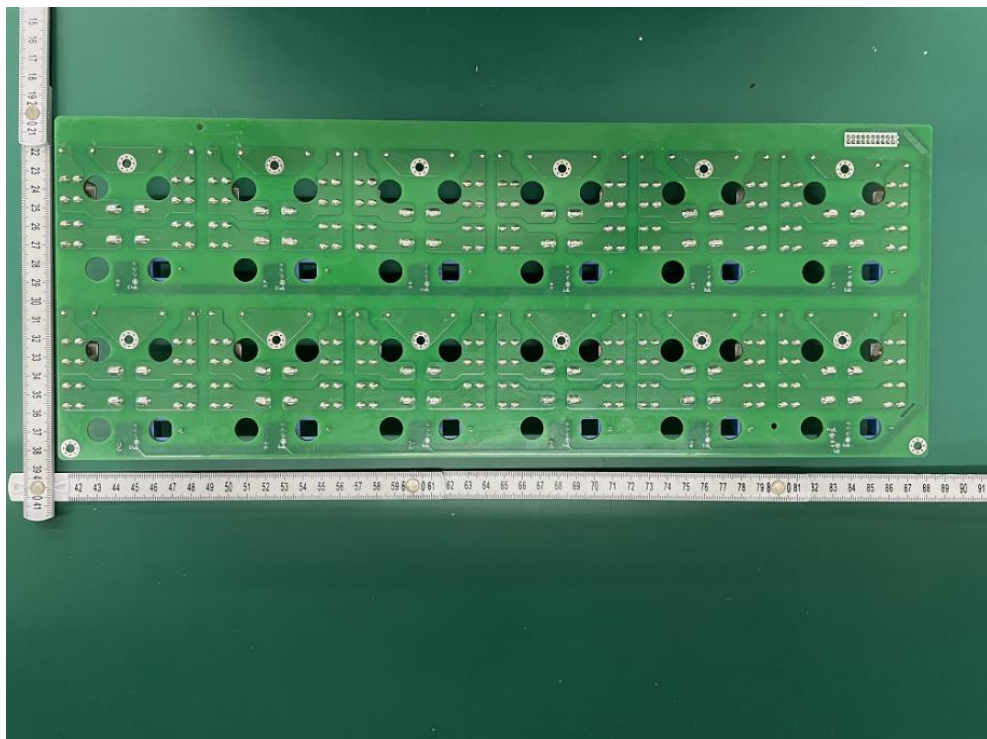


Figure 16. Back view of PV input board for C6-70K-T12-LV-40

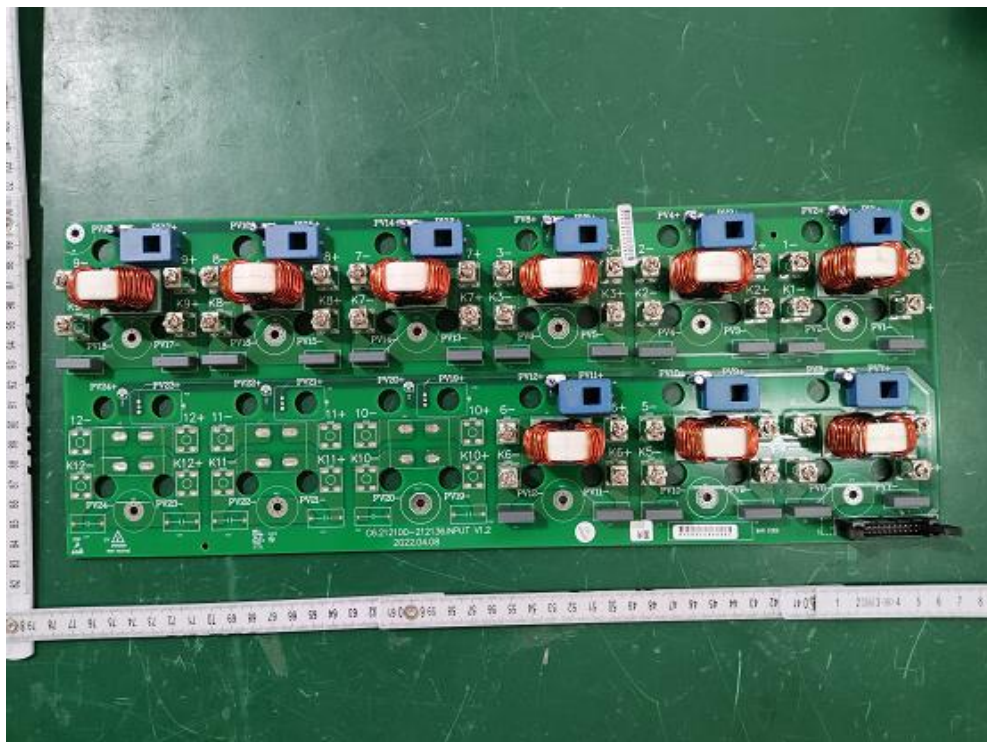


Figure 17. Front view of PV input board for C6-60K-T9-LV-40

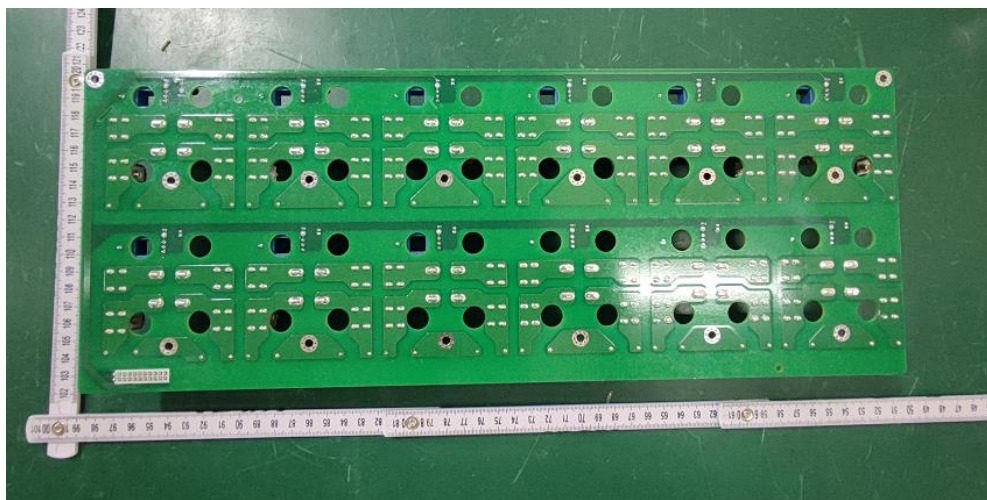


Figure 18. Back view of PV input board for C6-60K-T9-LV-40

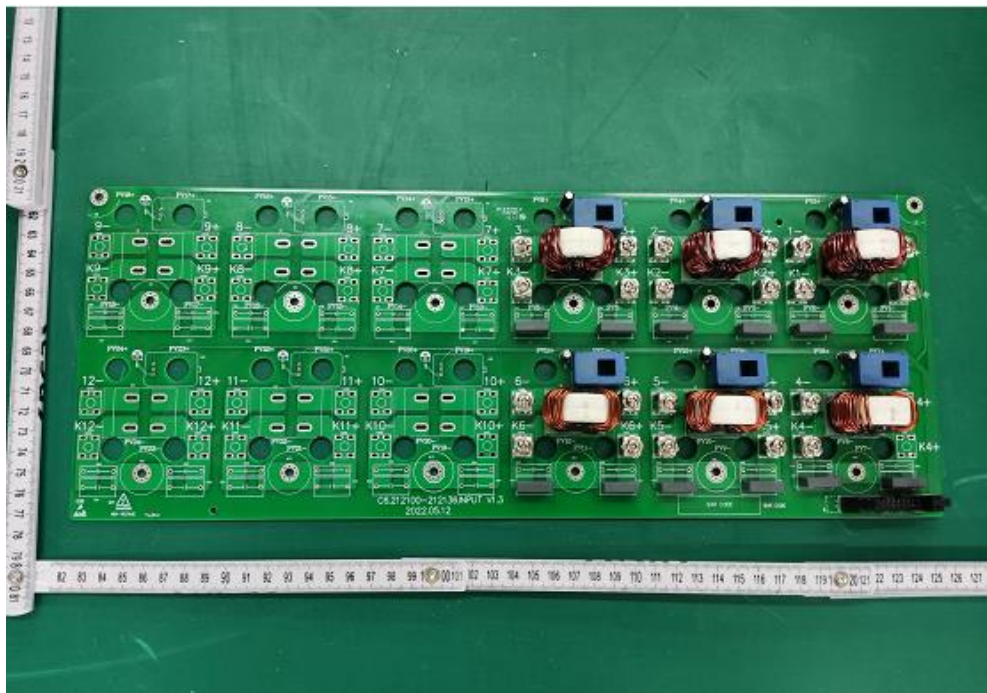


Figure 19. Front view of PV input board for C6-50K-T6-LV-40

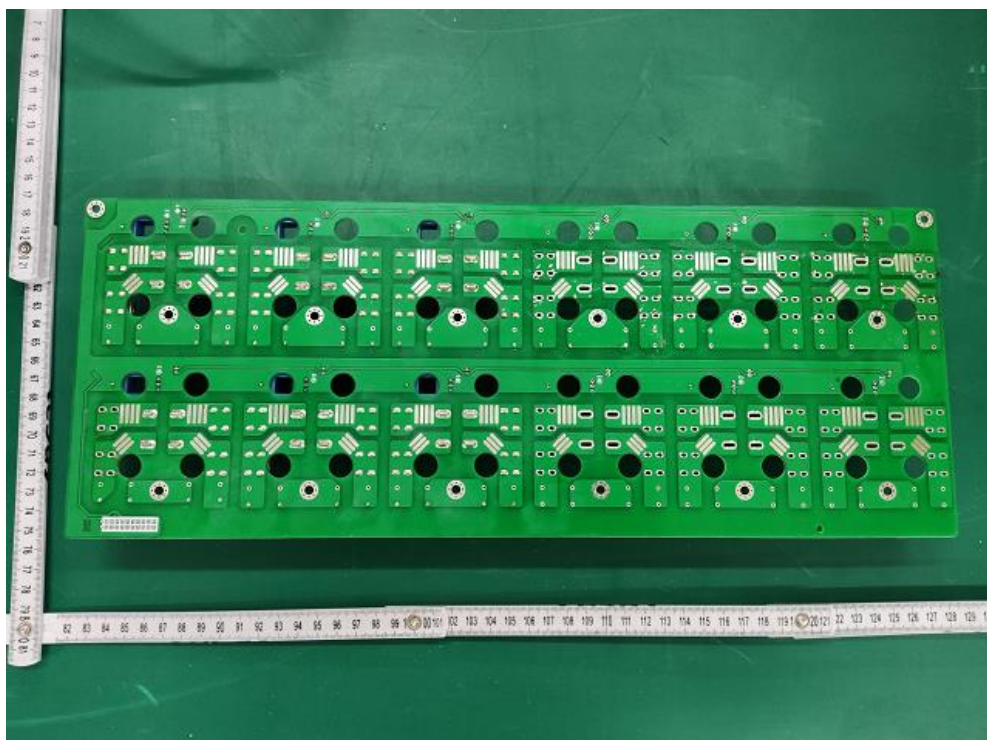


Figure 20. Back view of PV input board for C6-50K-T6-LV-40

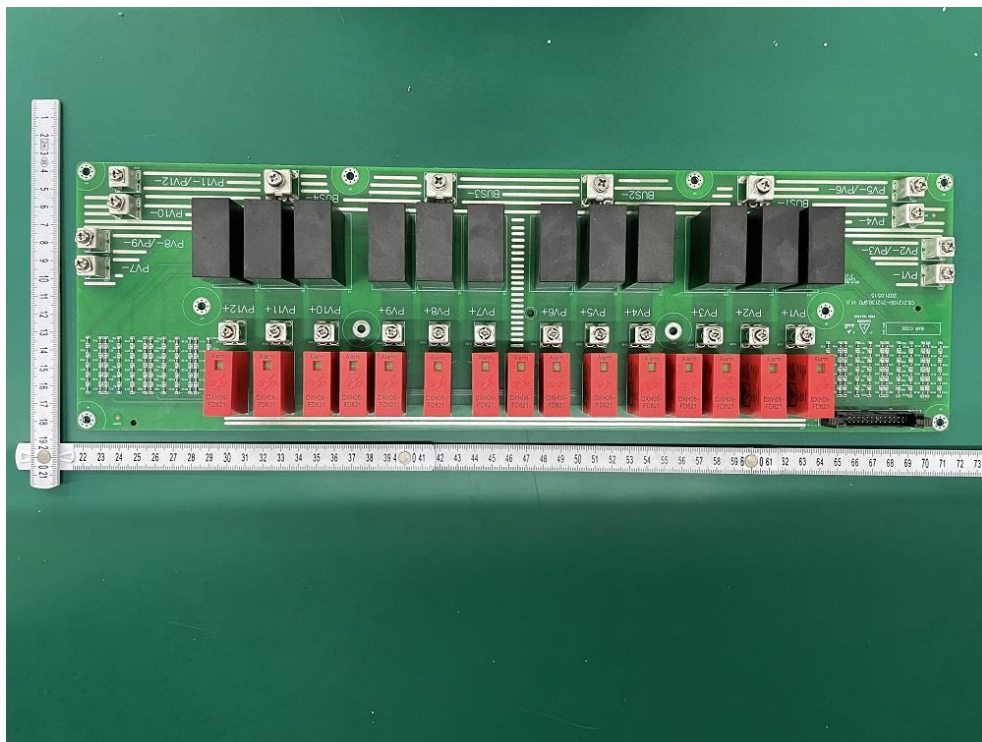


Figure 21. Front view of SPD board

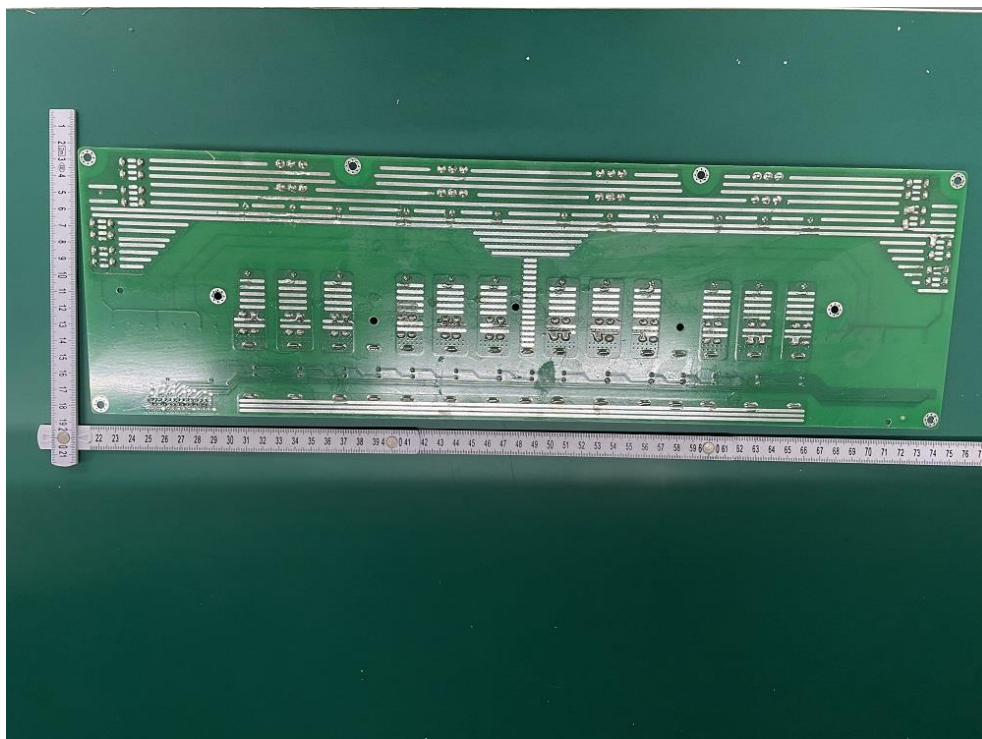


Figure 22. Back view of SPD board



Figure 23. Front view of CAP board

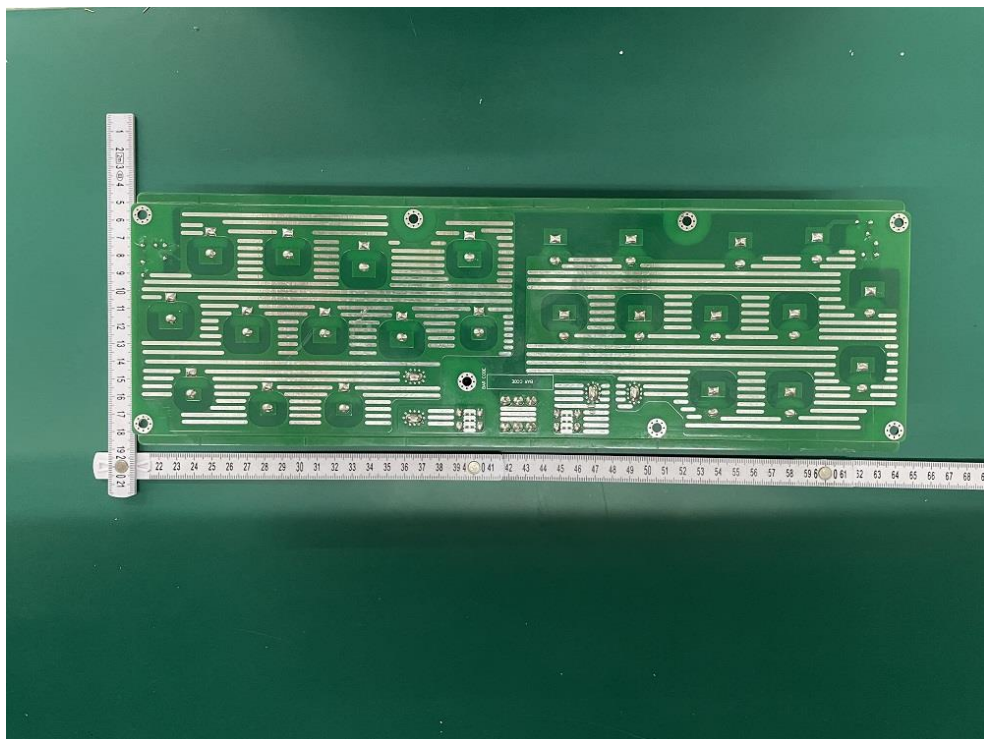


Figure 24. Back view of CAP board

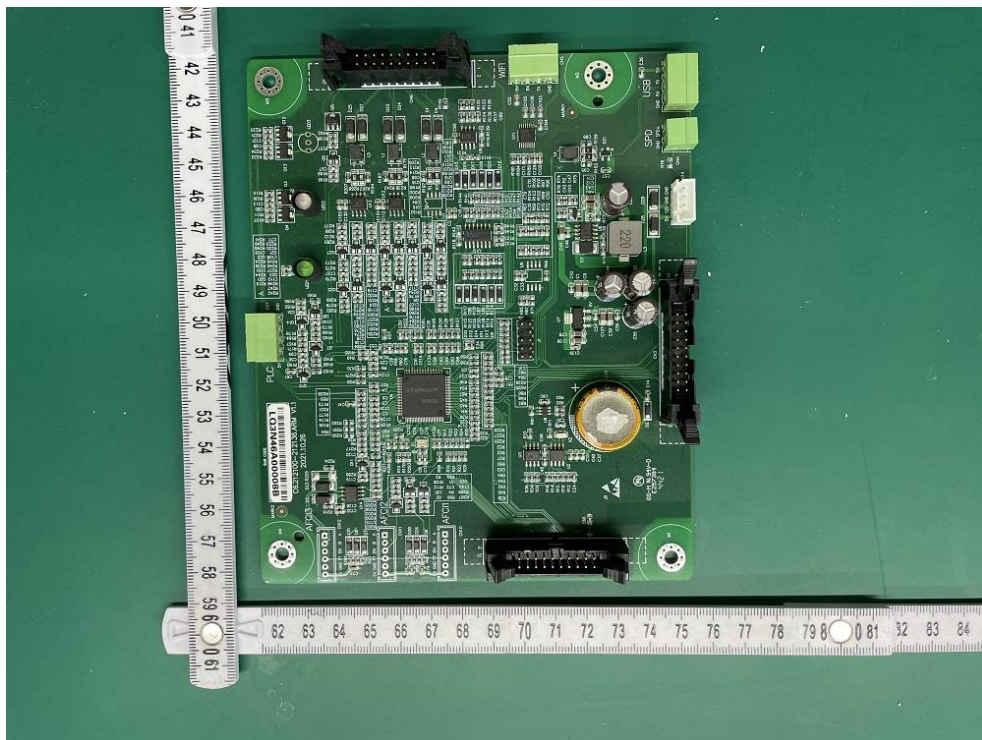


Figure 25. Front view of ARM board

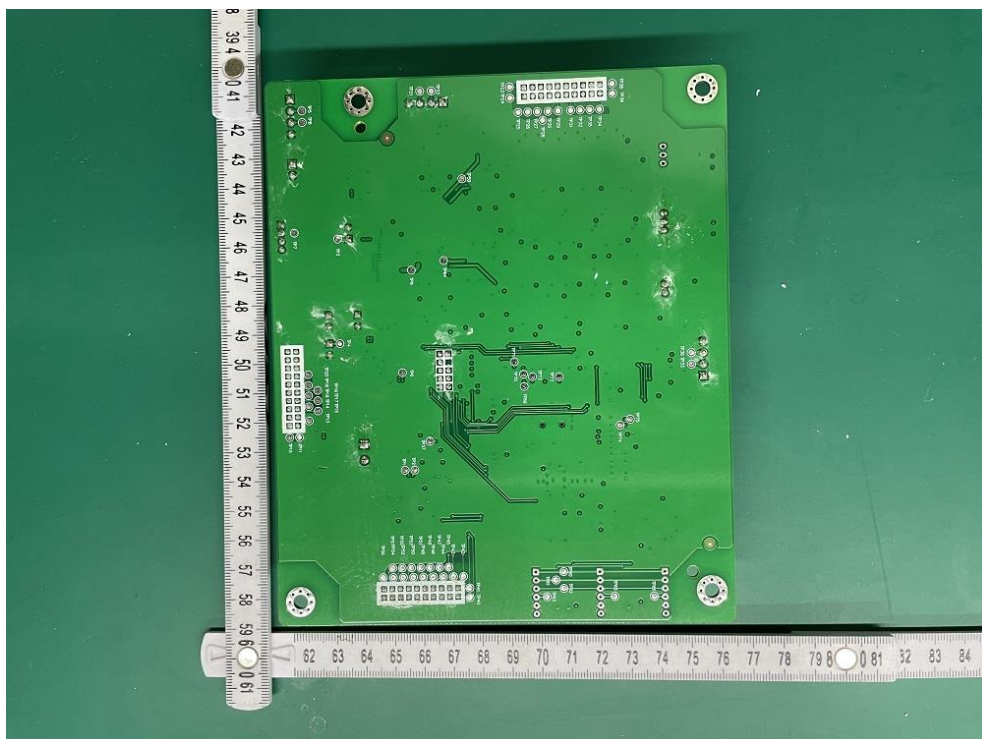


Figure 26. Back view of ARM board

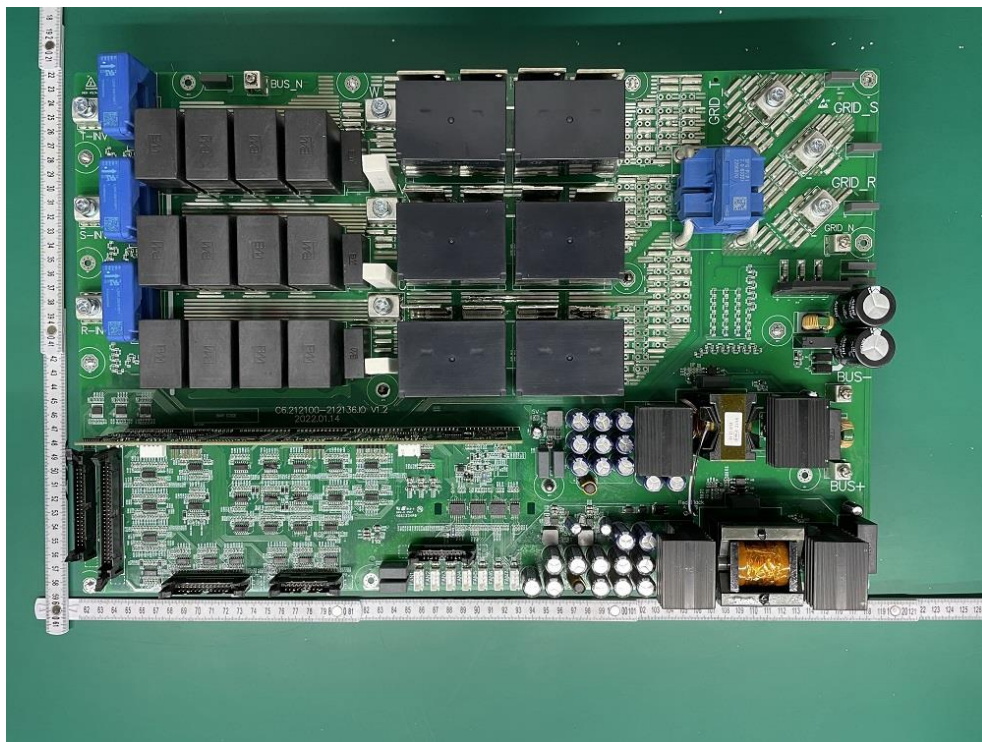


Figure 27. Front view of IO board

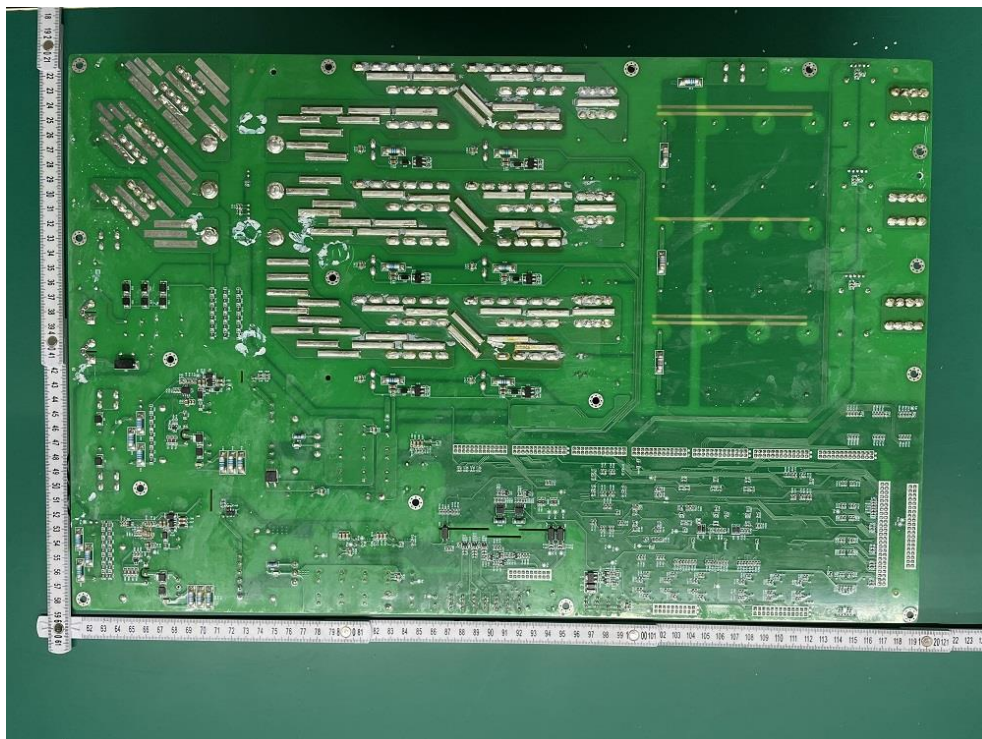


Figure 28. Back view of IO board

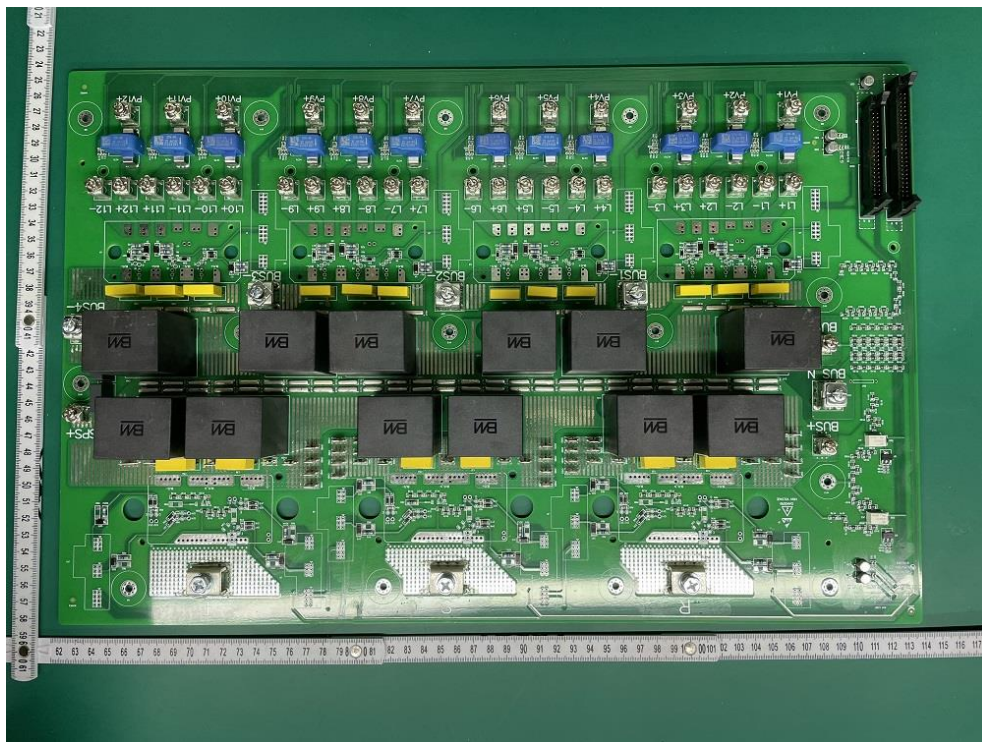


Figure 29. Front view of Main power board for C6-70K-T12-LV-40

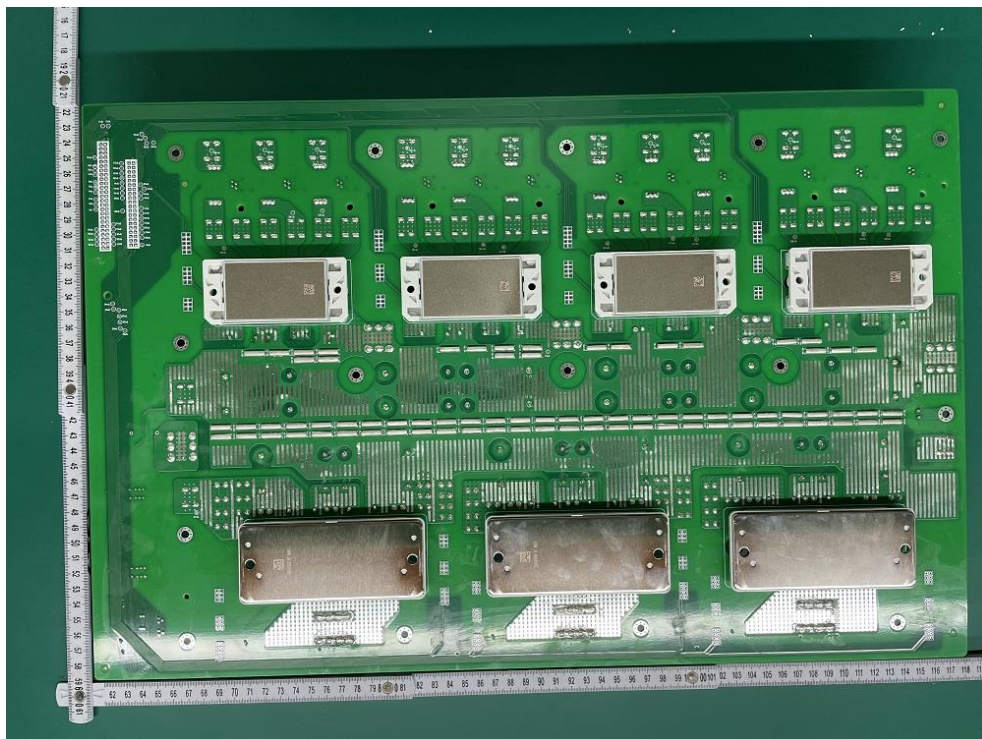


Figure 30. Back view of Main power board for C6-70K-T12-LV-40

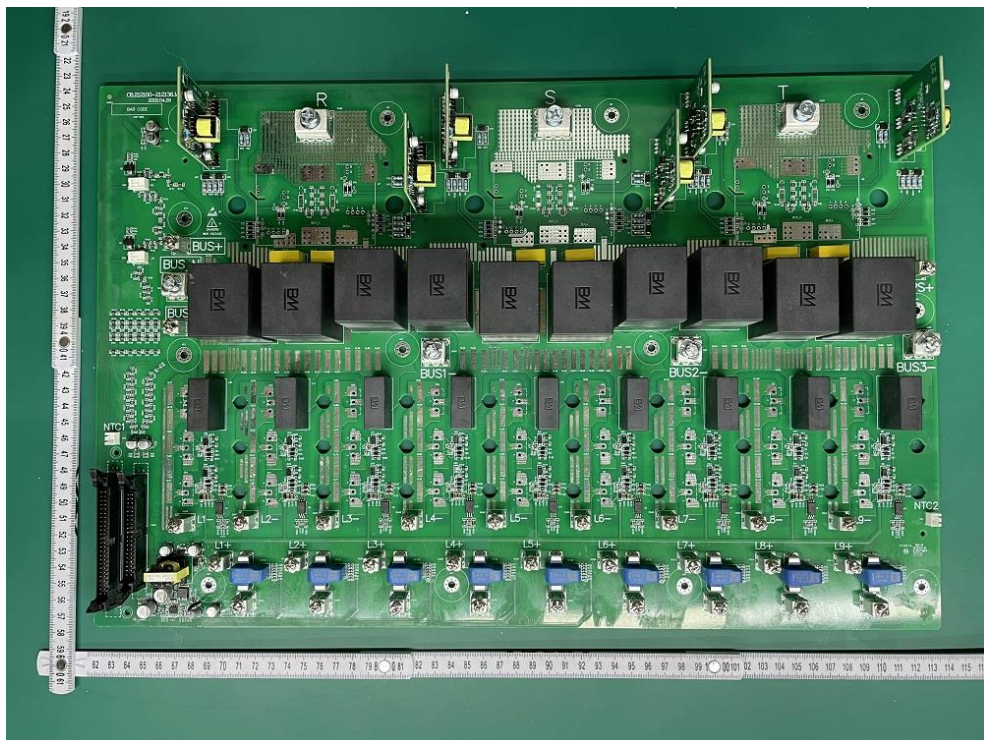


Figure 31. Front view of Main power board for C6-50K-T6-LV-40, C6-60K-T9-LV-40

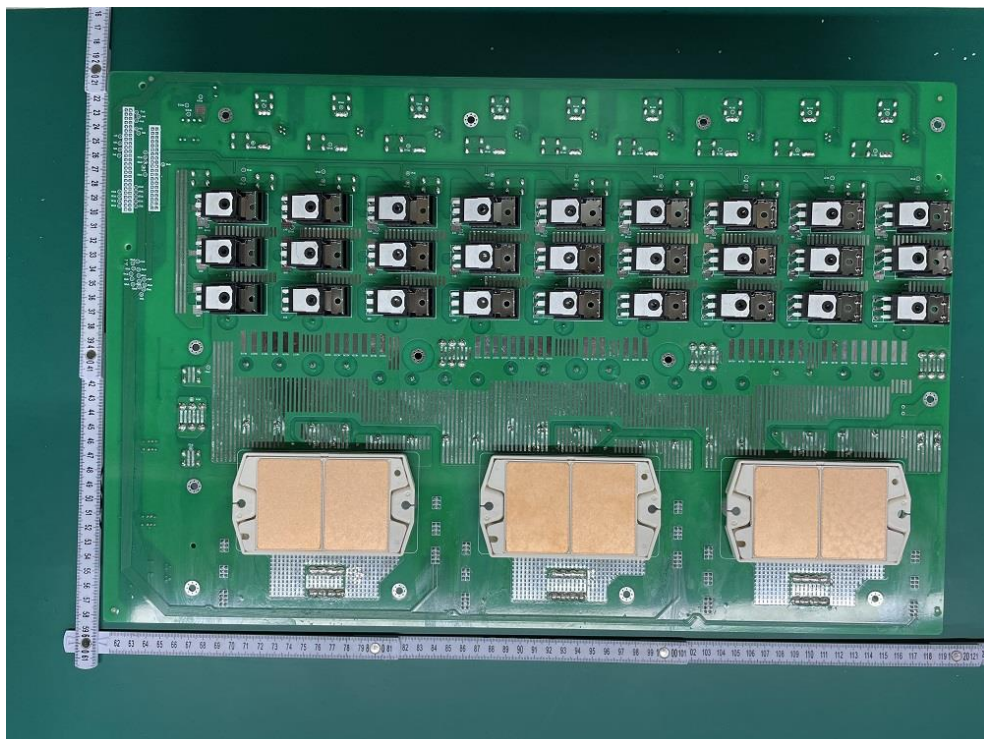


Figure 32. Back view of Main power board for C6-50K-T6-LV-40, C6-60K-T9-LV-40

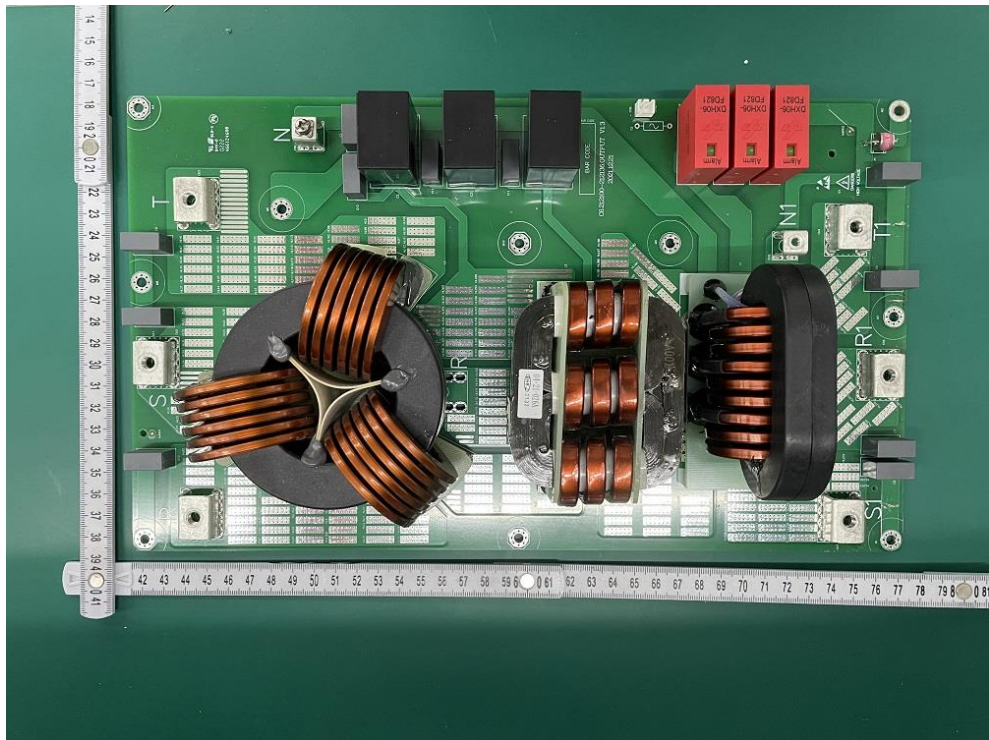


Figure 33. Front view of AC output board

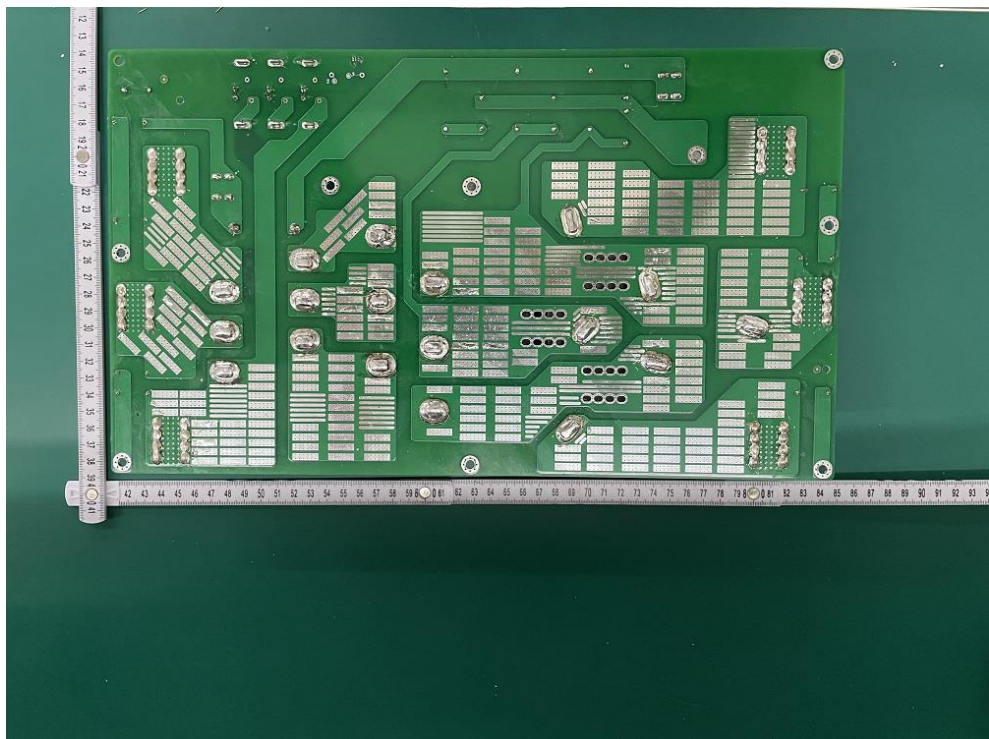


Figure 34. Back view of AC output board

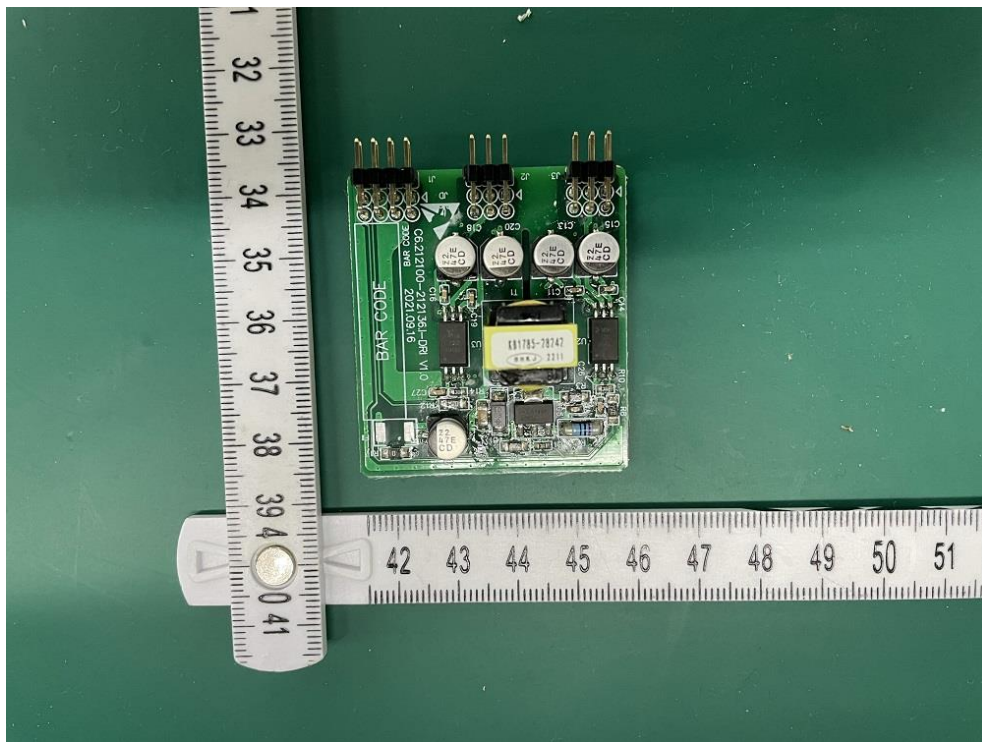


Figure 35. Front view of INV drive board

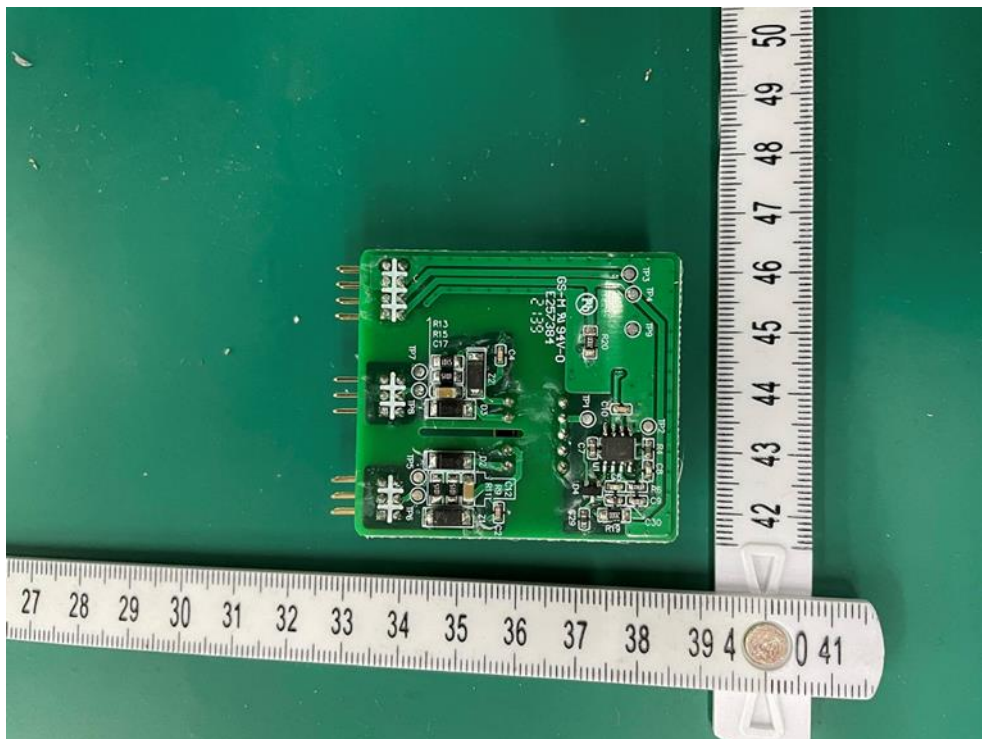


Figure 36. Back view of INV drive board

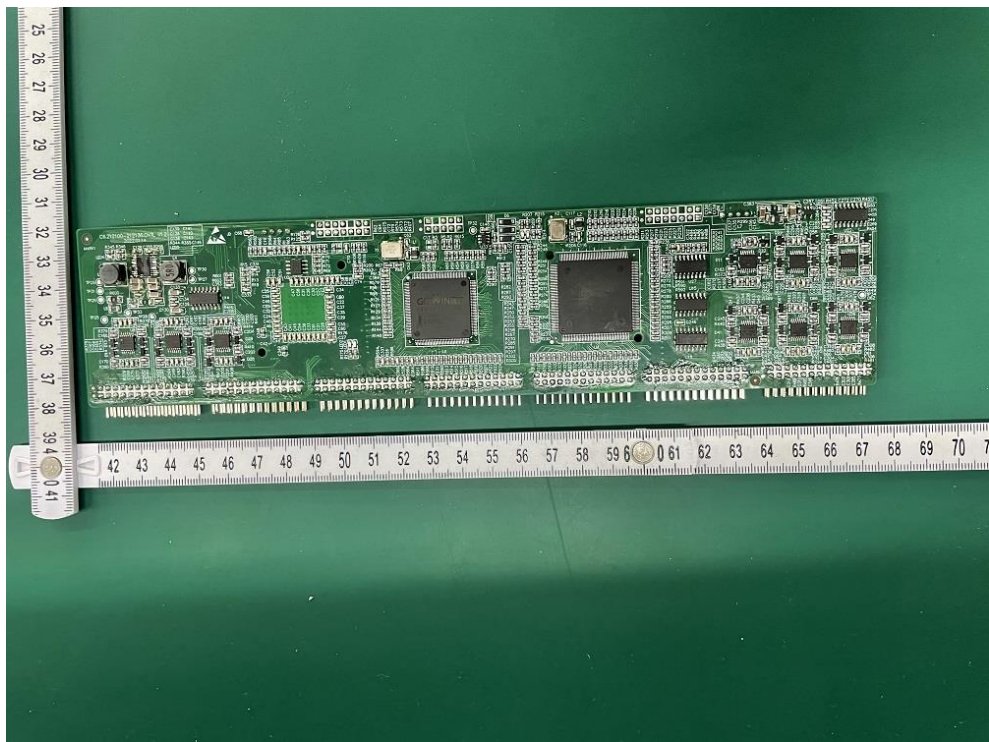


Figure 37. Front view of Control board

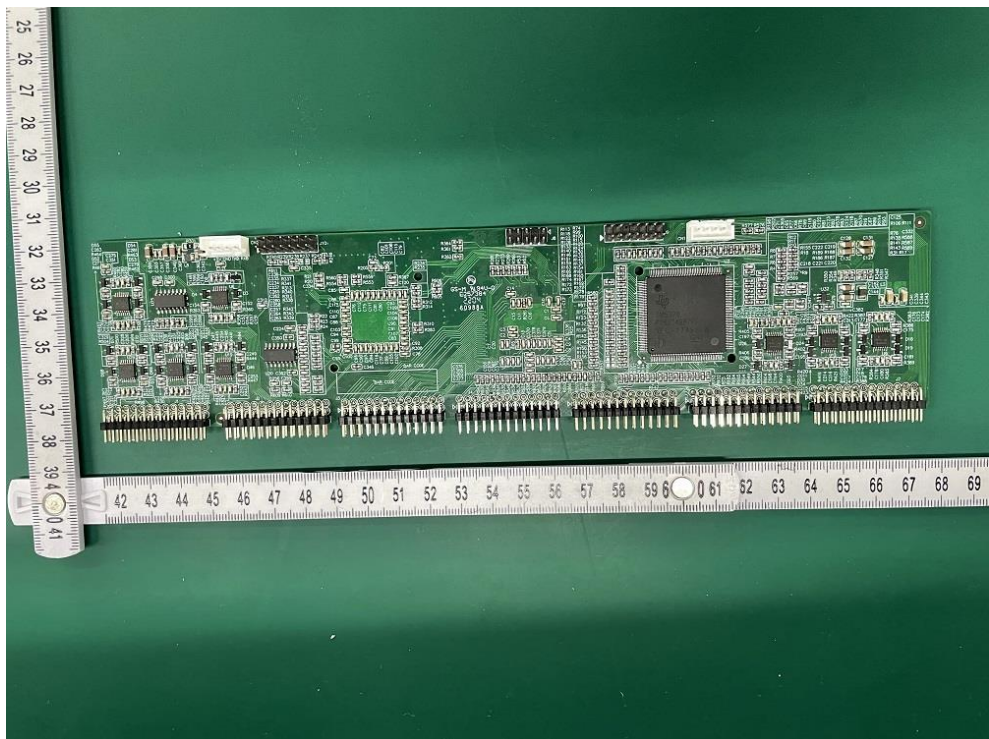


Figure 38. Back view of Control board

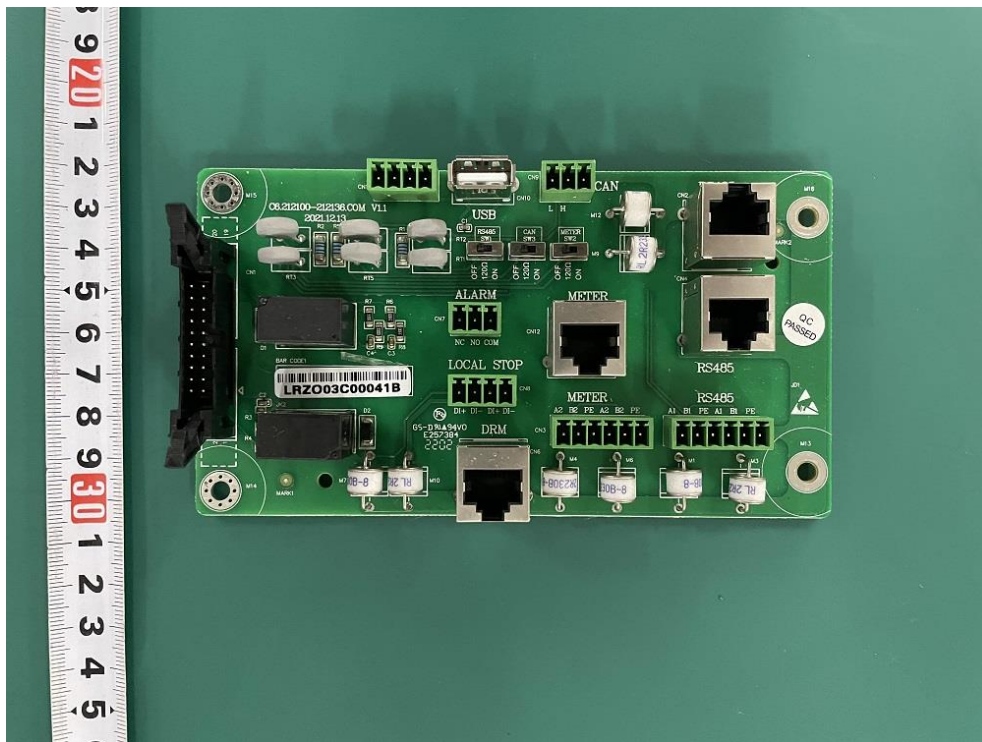


Figure 39. Front view of Communication board

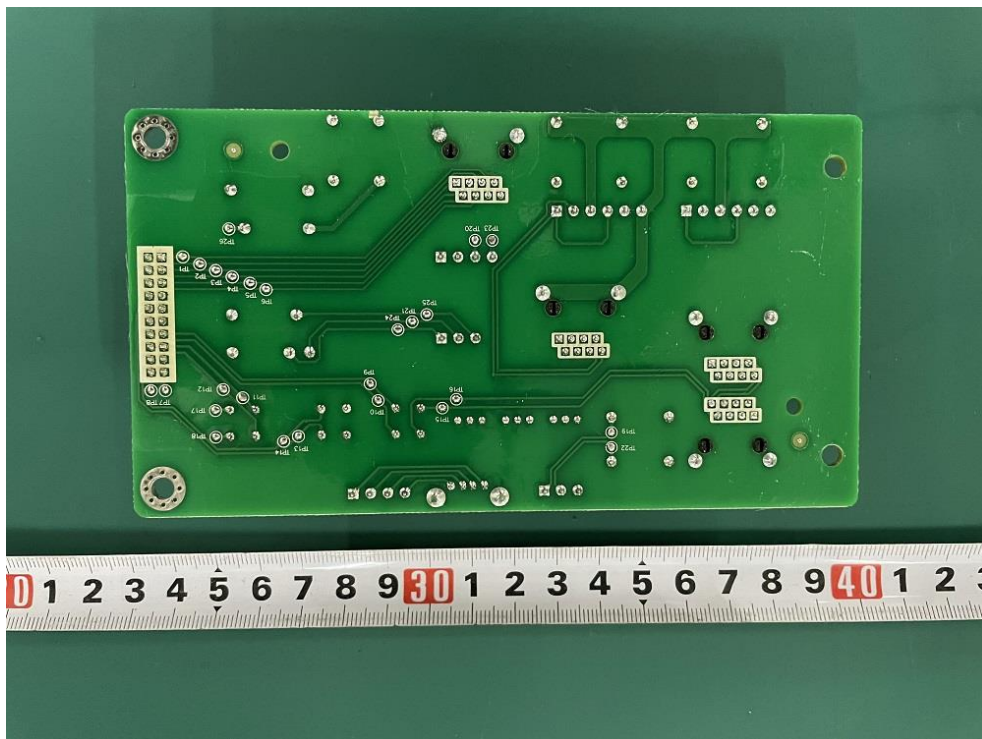


Figure 40. Back view of Communication board

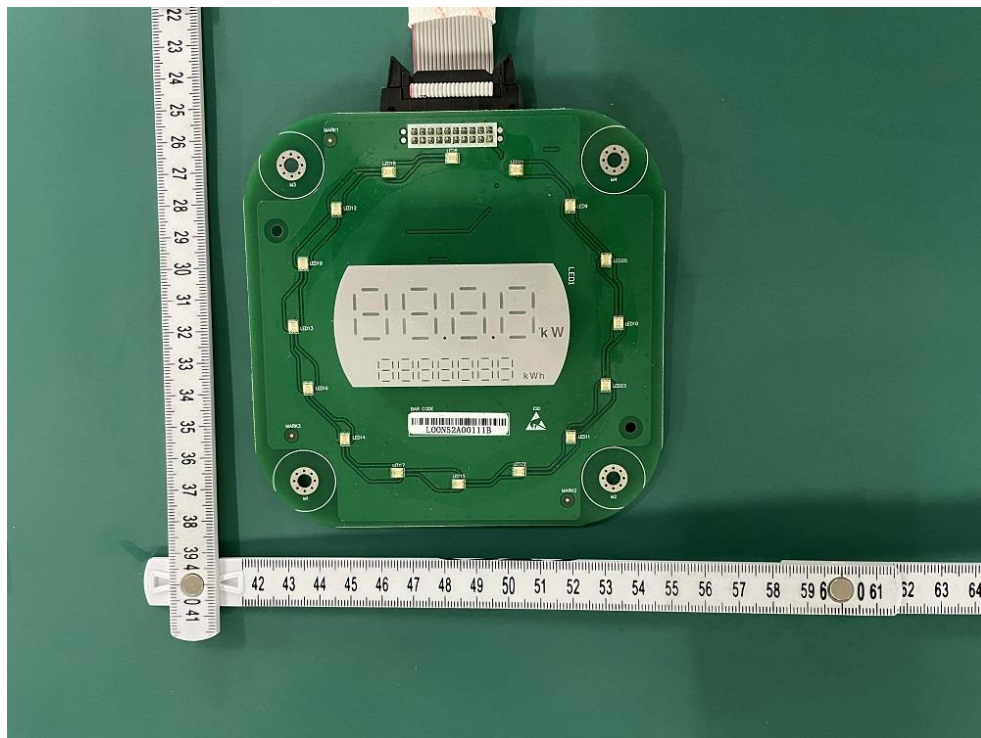


Figure 41. Front view of Display board

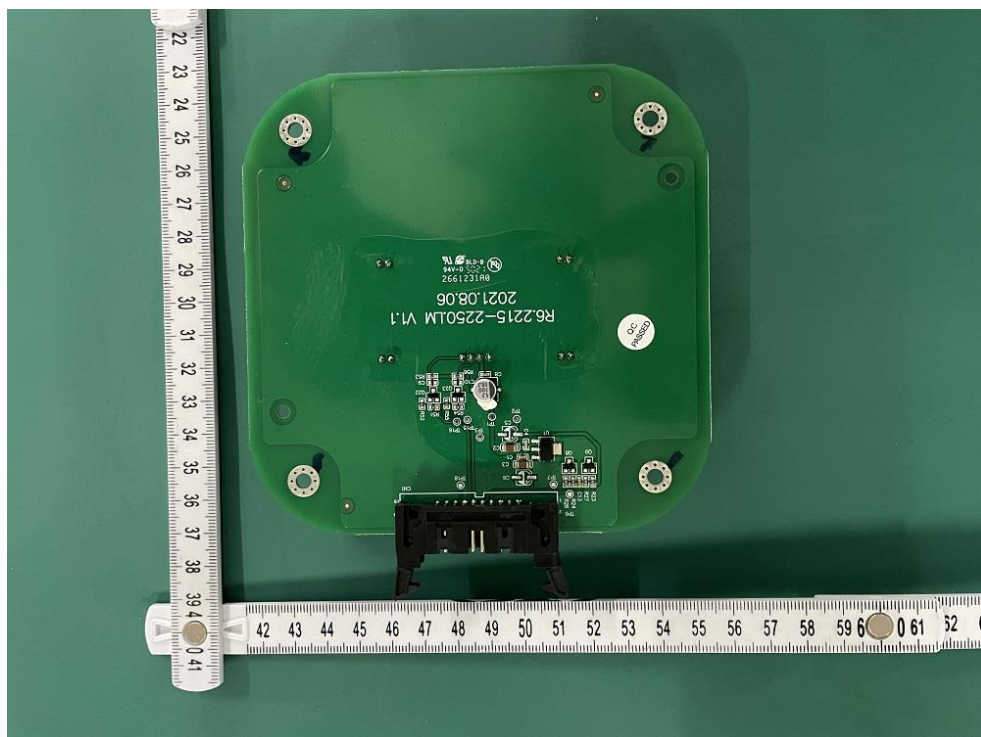


Figure 42. Back view of Communication board

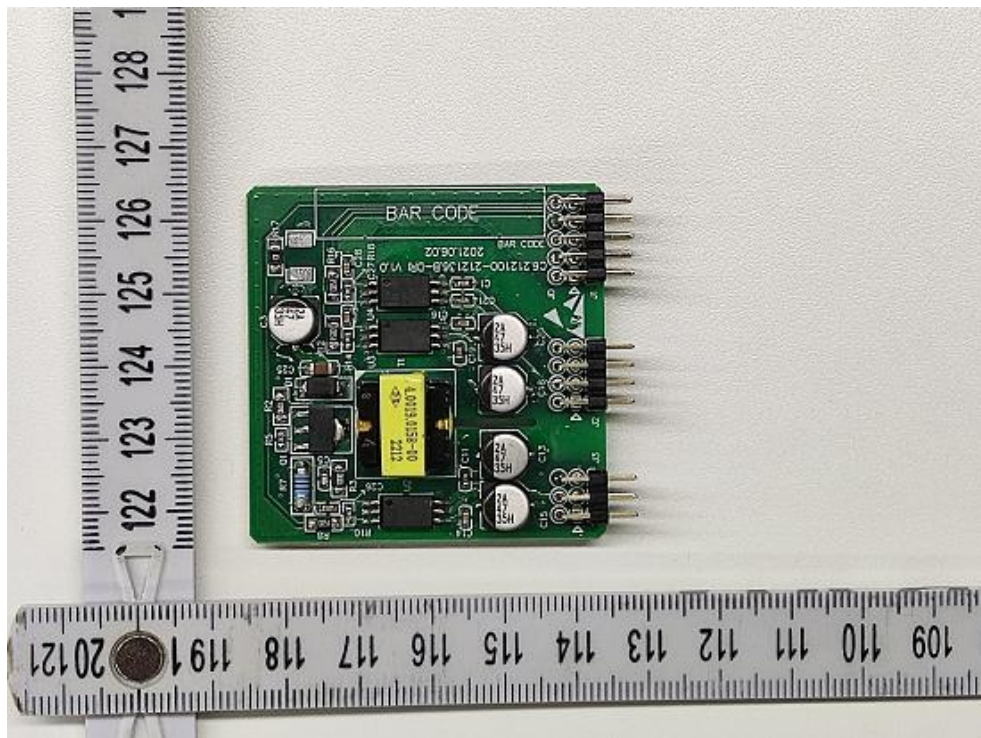


Figure 43. Front view of Boost drive board

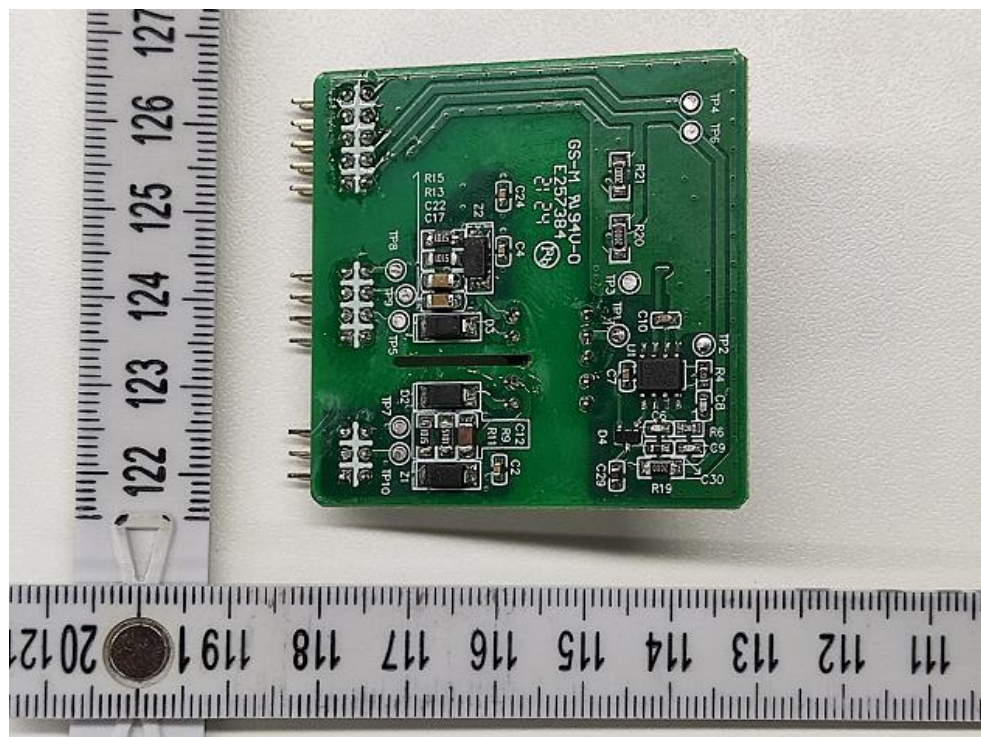


Figure 44. Back view of Boost drive board



Figure 45. Internal ground view



Figure 46. External ground view